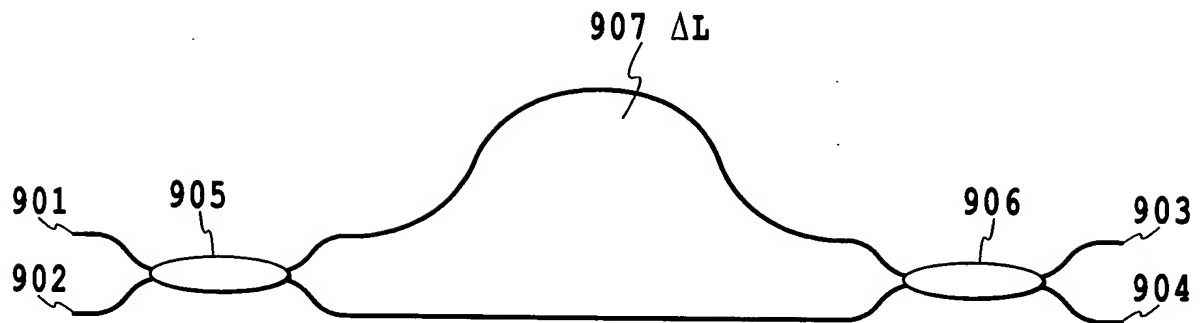
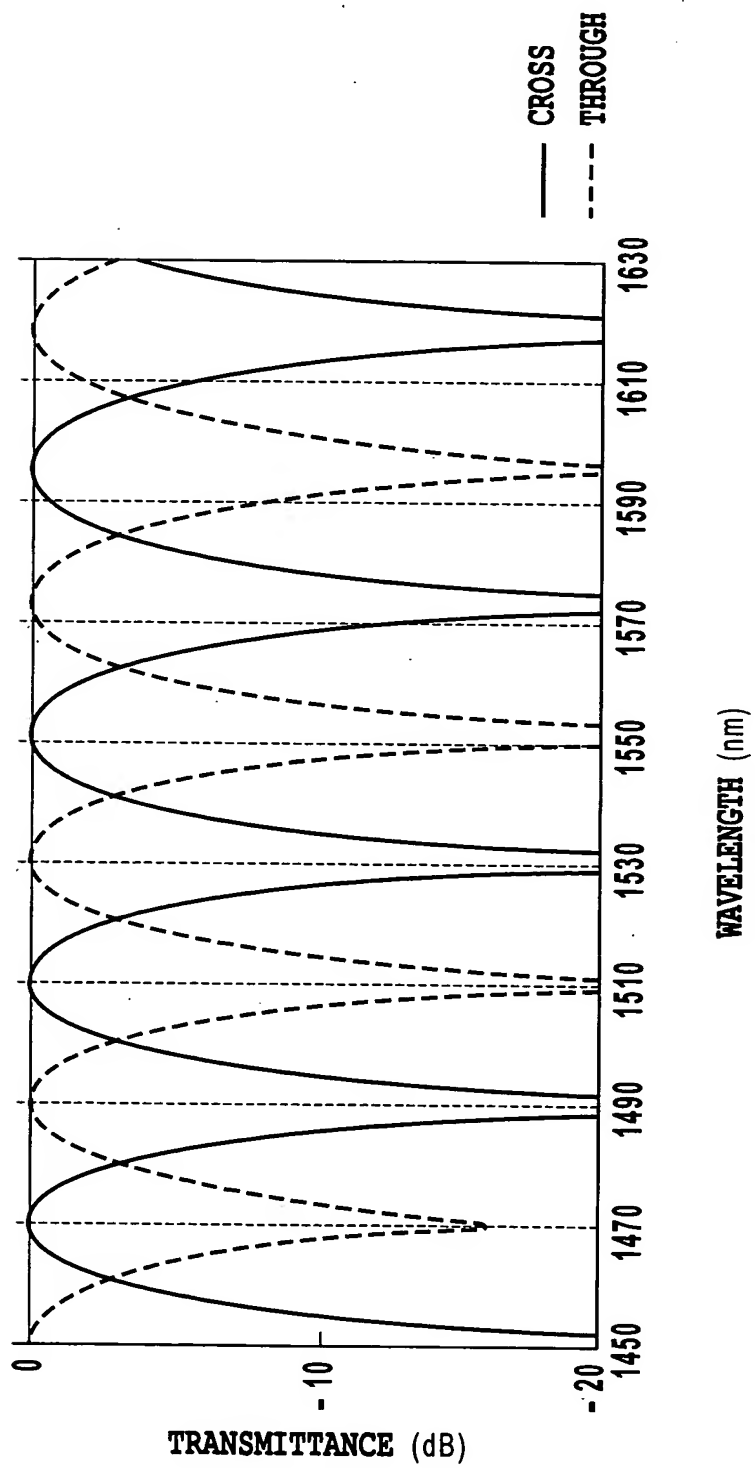


1/80



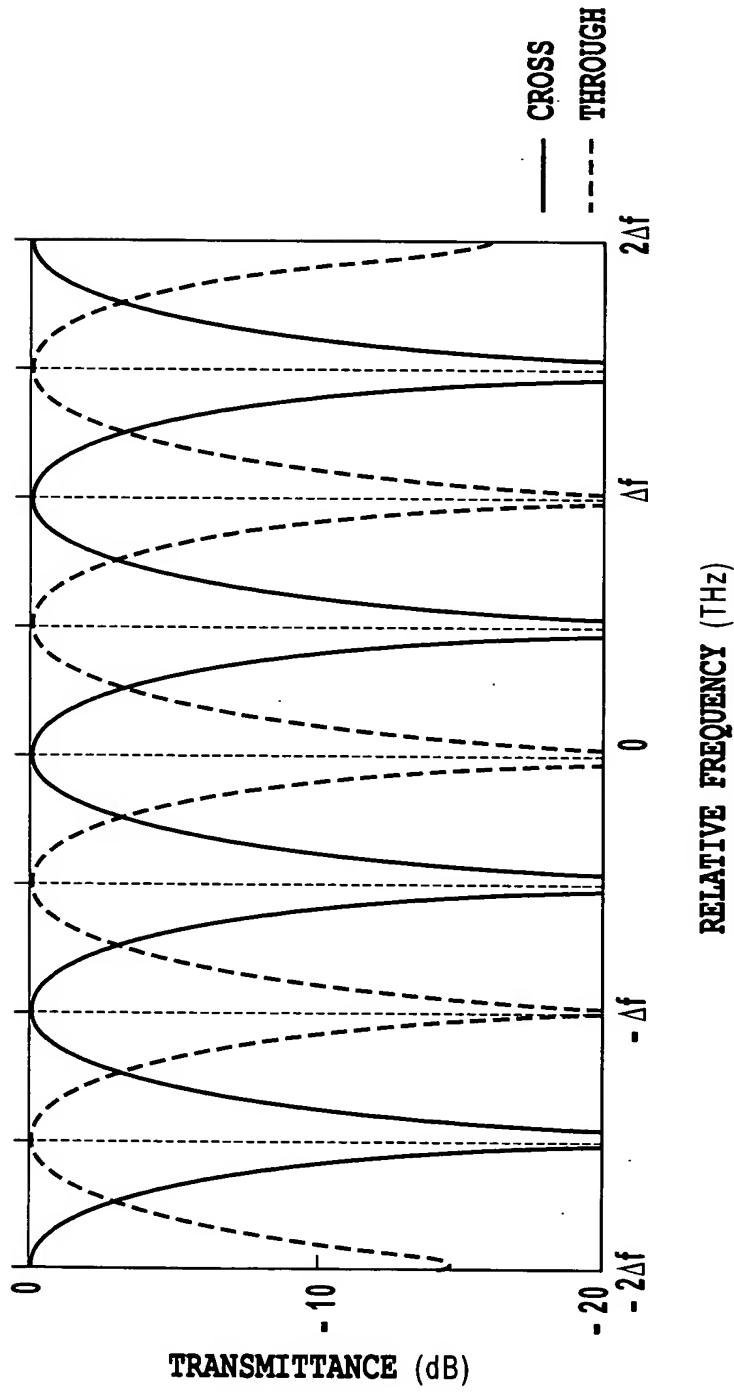
RELATED ART
FIG.1

2/80



RELATED ART

FIG.2



RELATED ART

FIG. 3

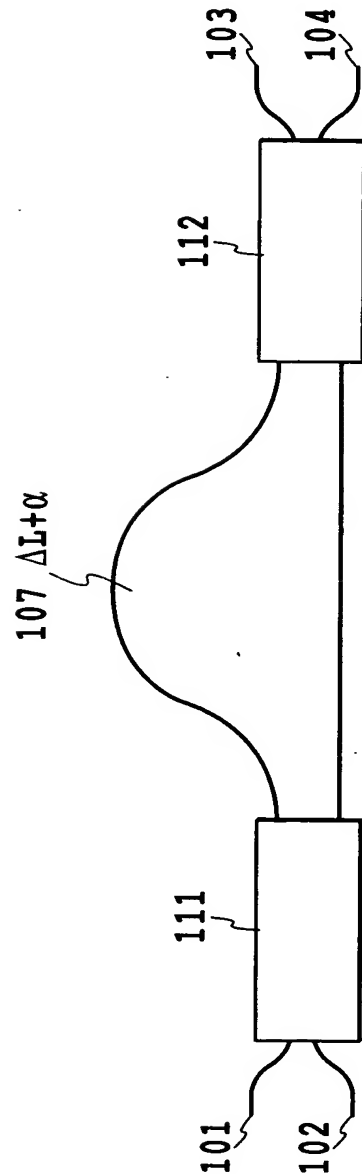


FIG.4

5/80

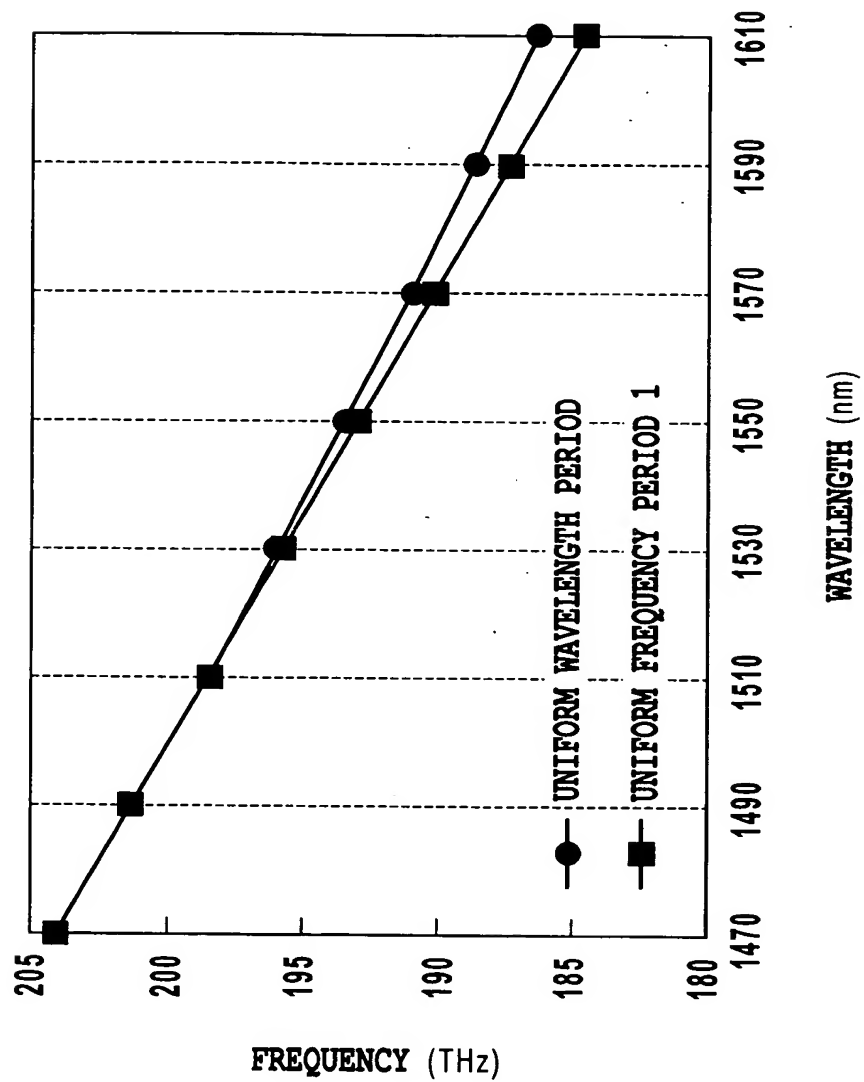


FIG.5

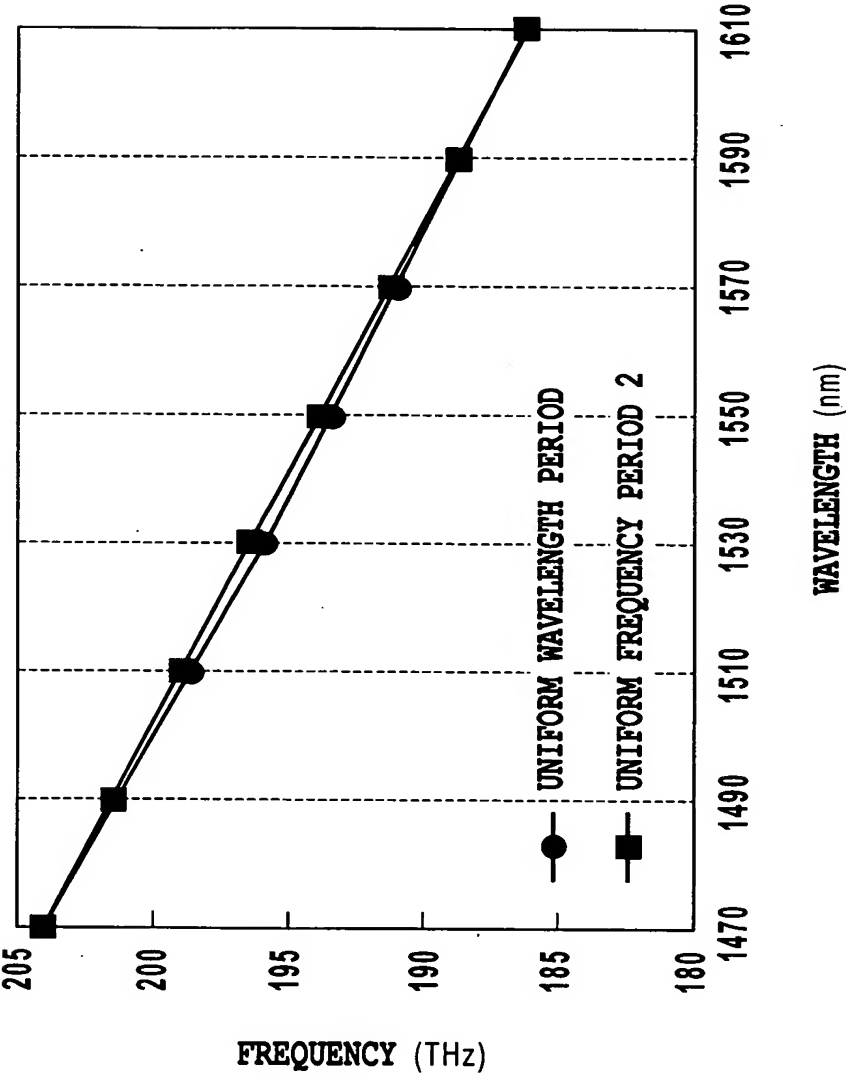


FIG.6

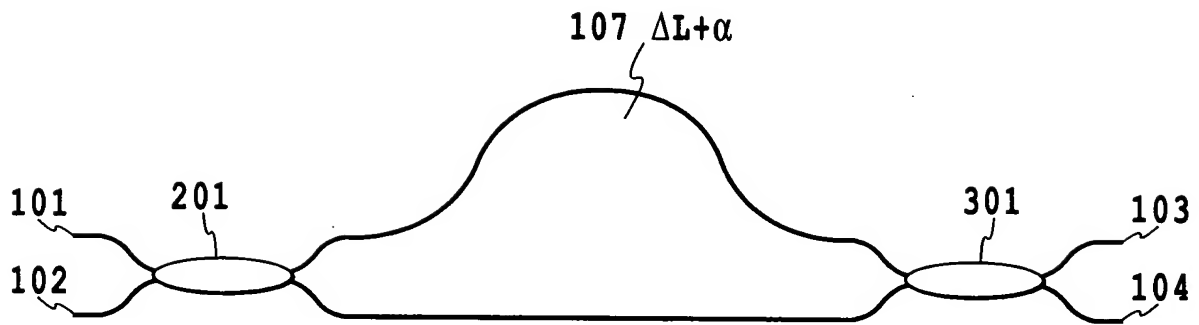


FIG.7

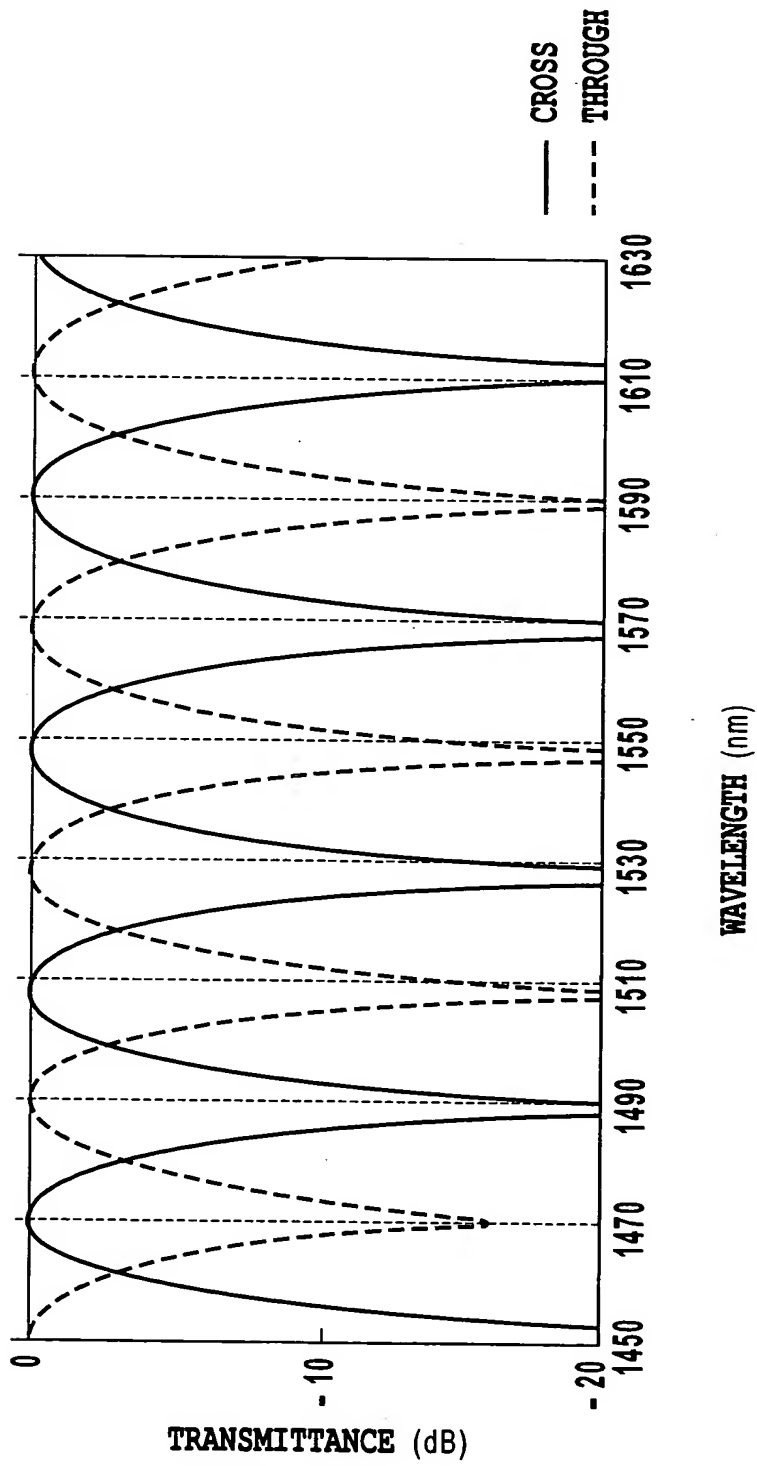


FIG.8

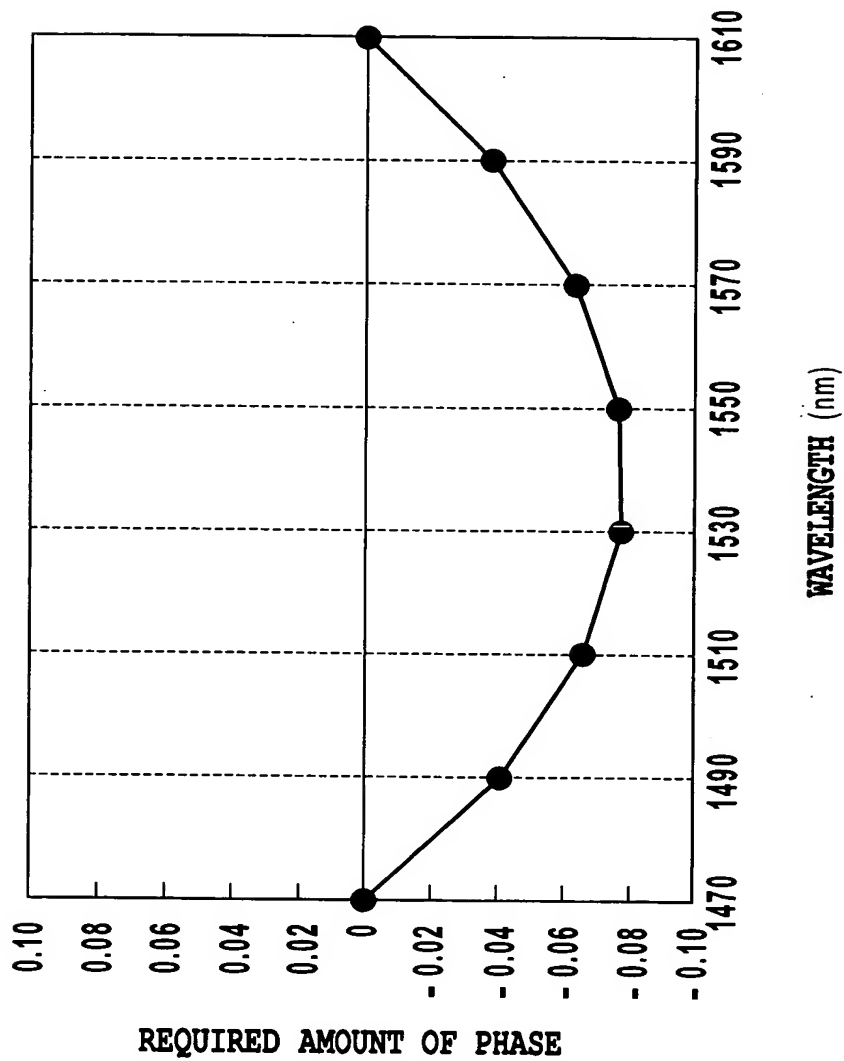


FIG.9

10/80

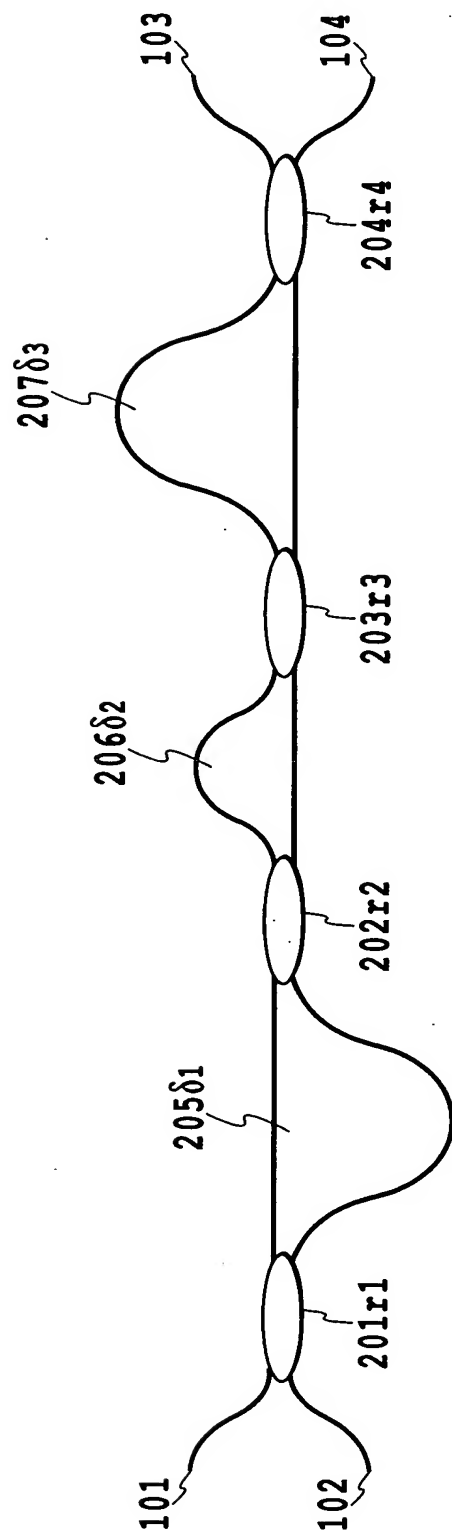


FIG.10

11/80

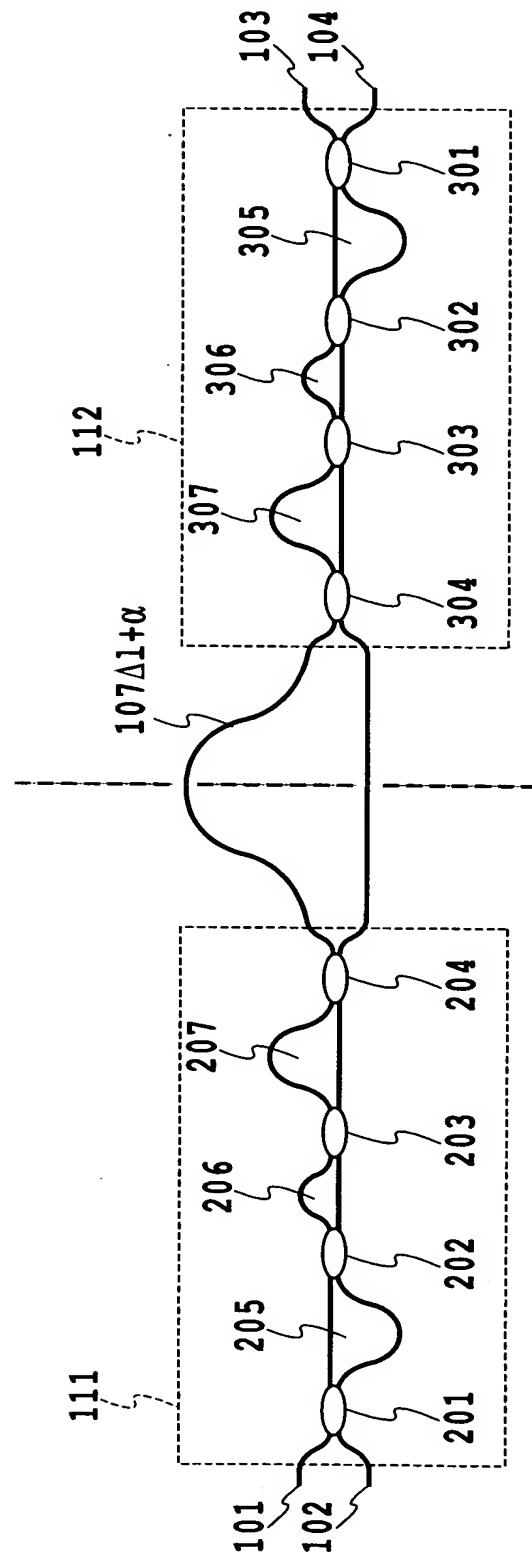


FIG.11

12/80

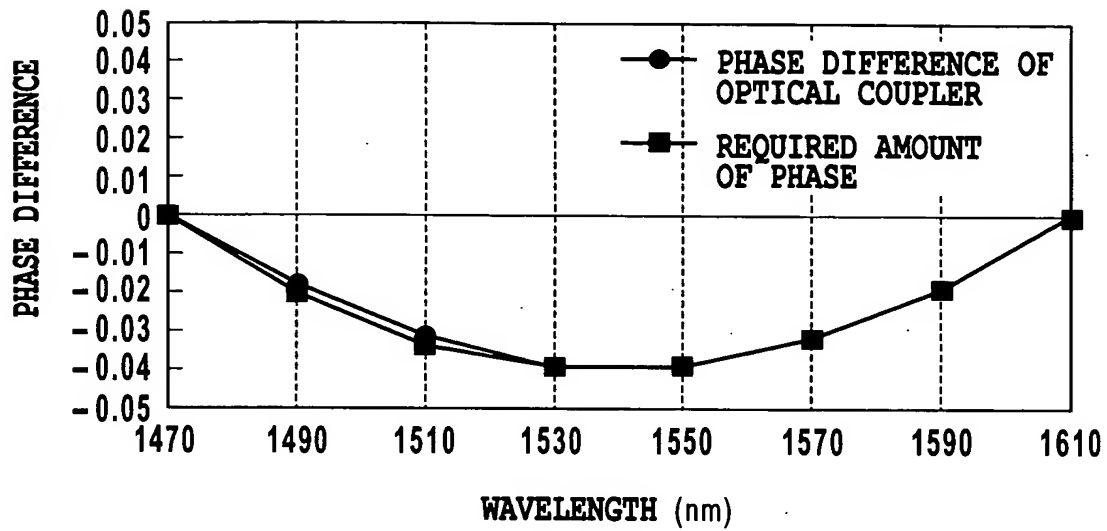


FIG.12A

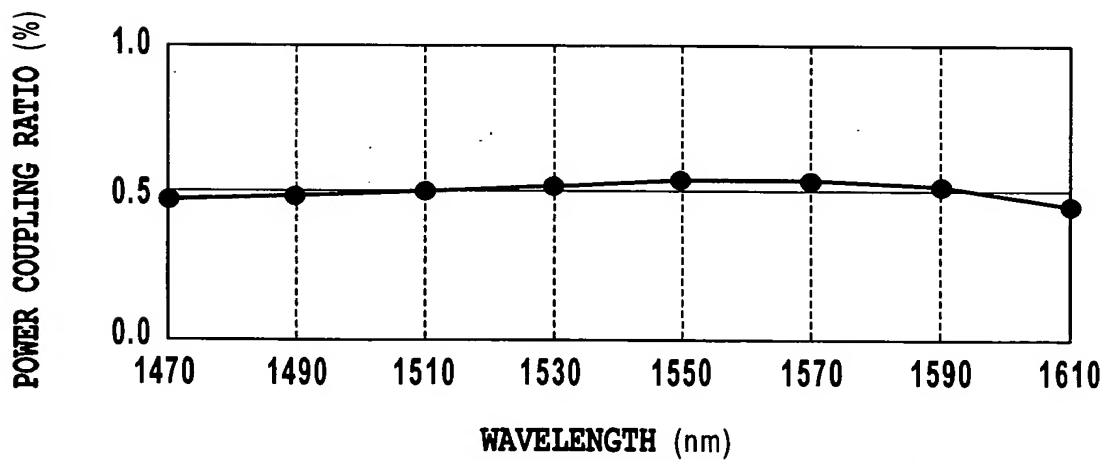


FIG.12B

13/80

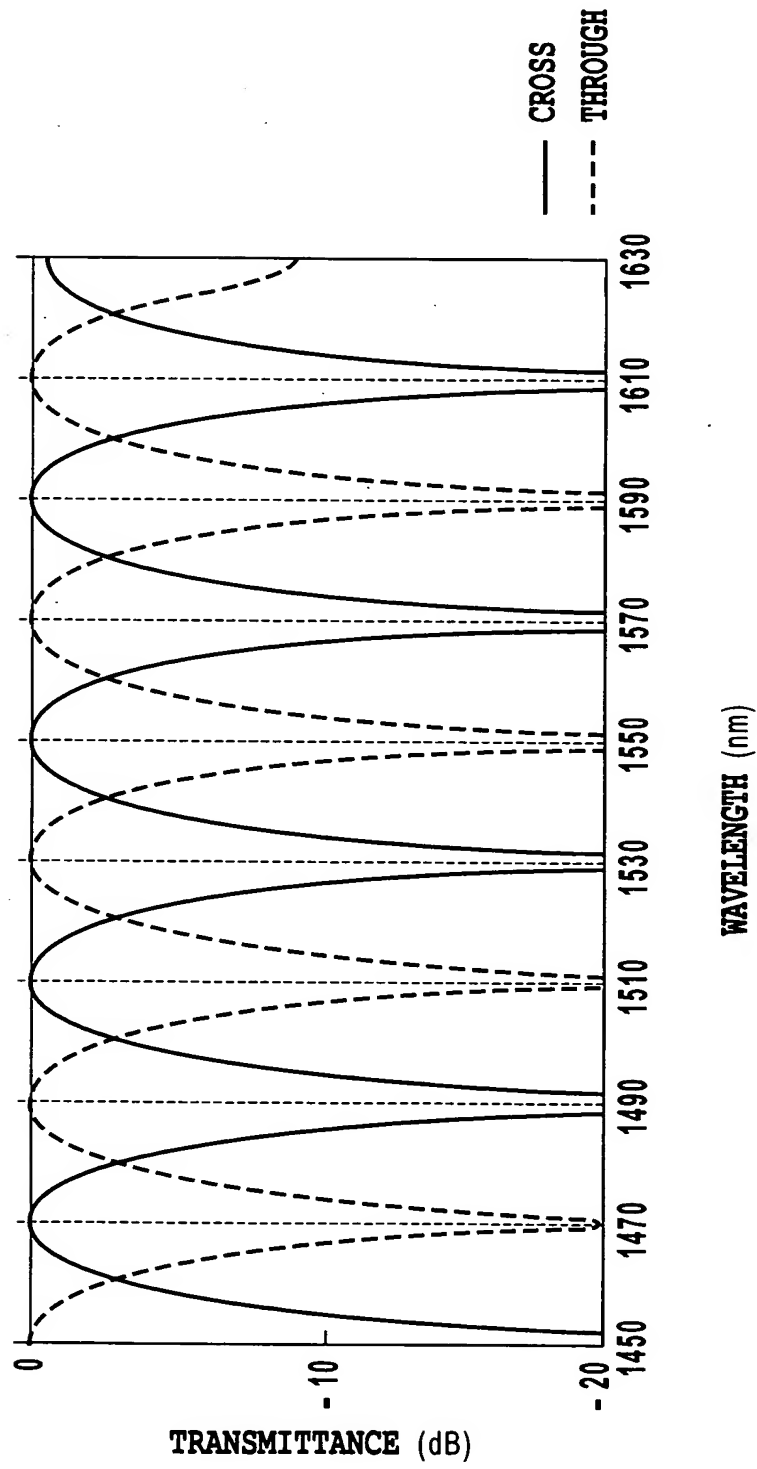


FIG.13

14/80

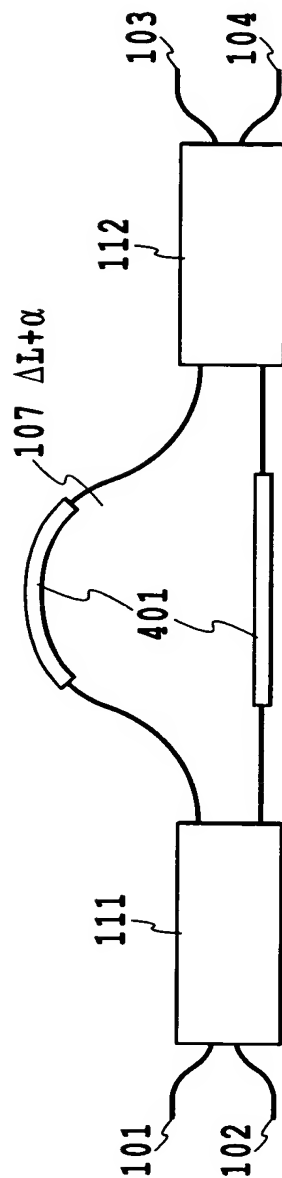


FIG.14



FIG. 15

16/80

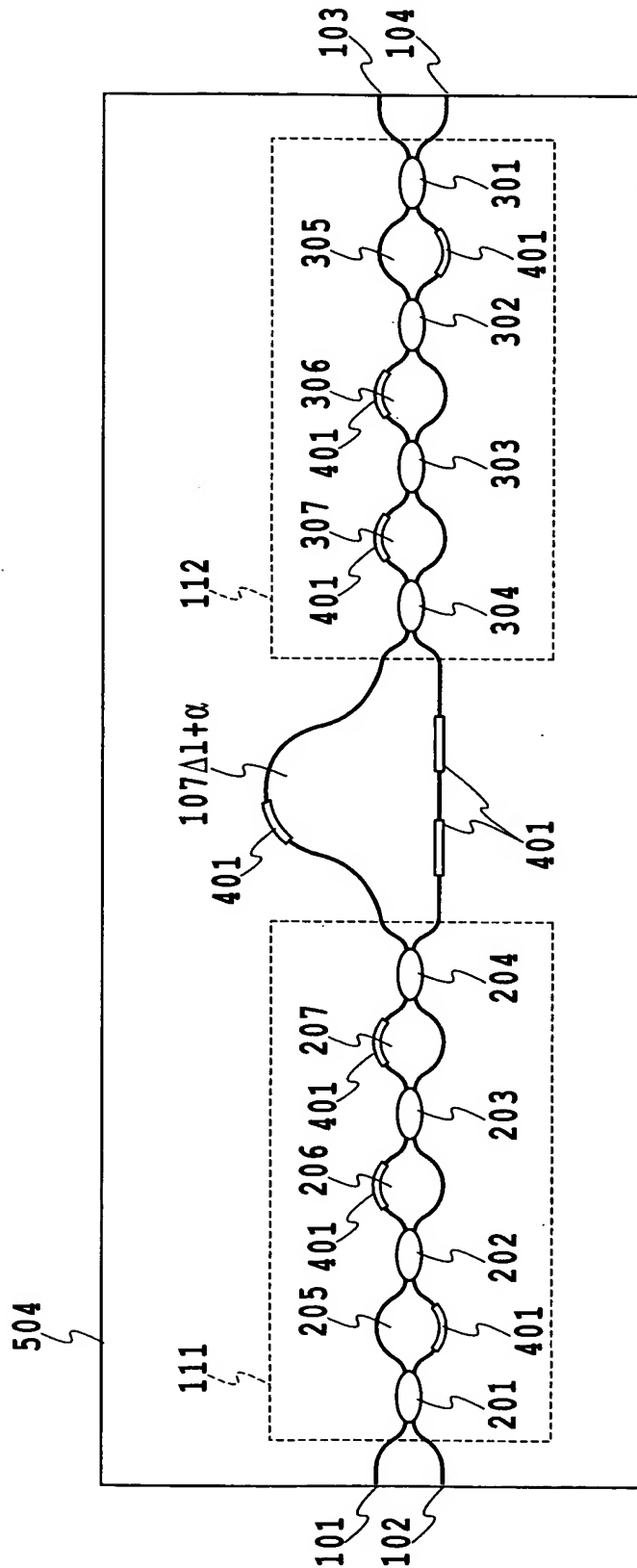


FIG.16

17/80

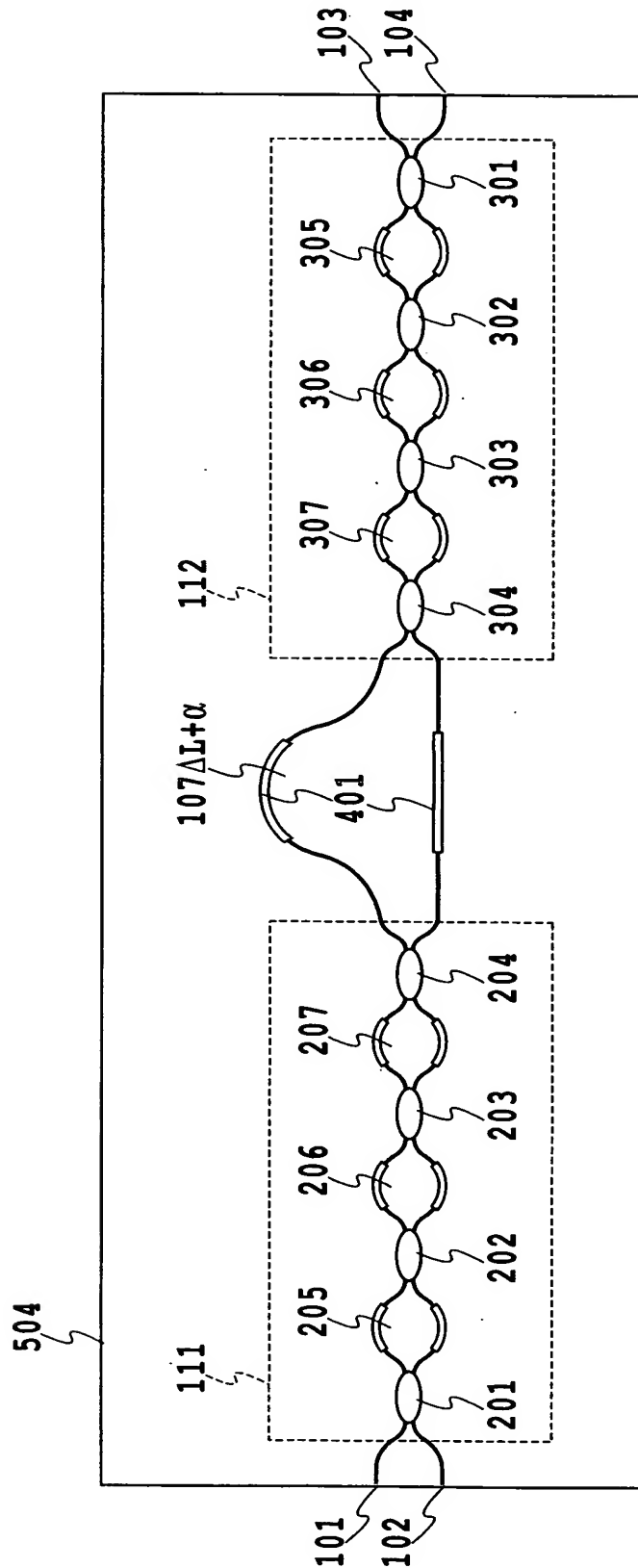


FIG.17

18/80

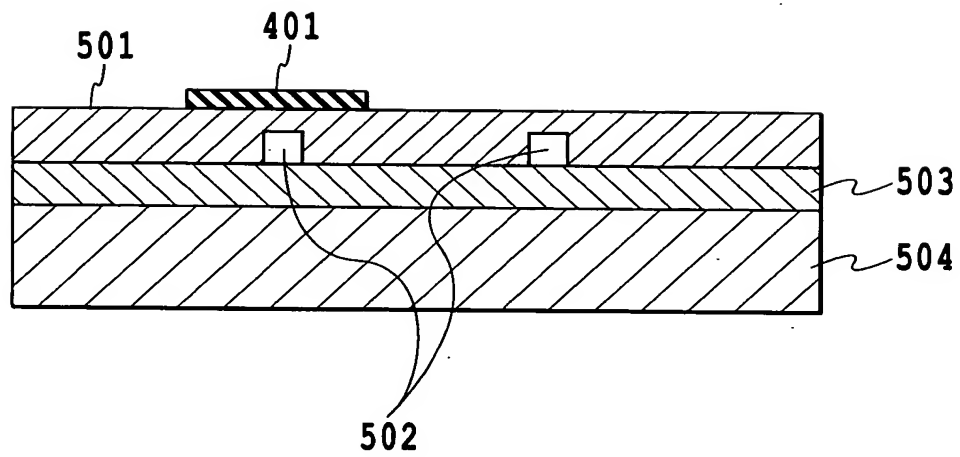


FIG.18

19/80

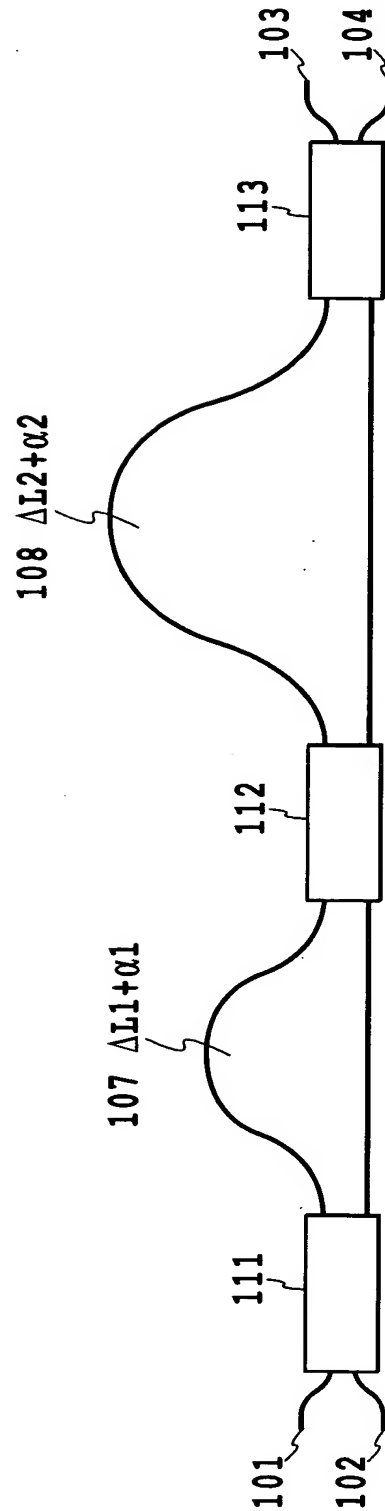


FIG.19

20/80

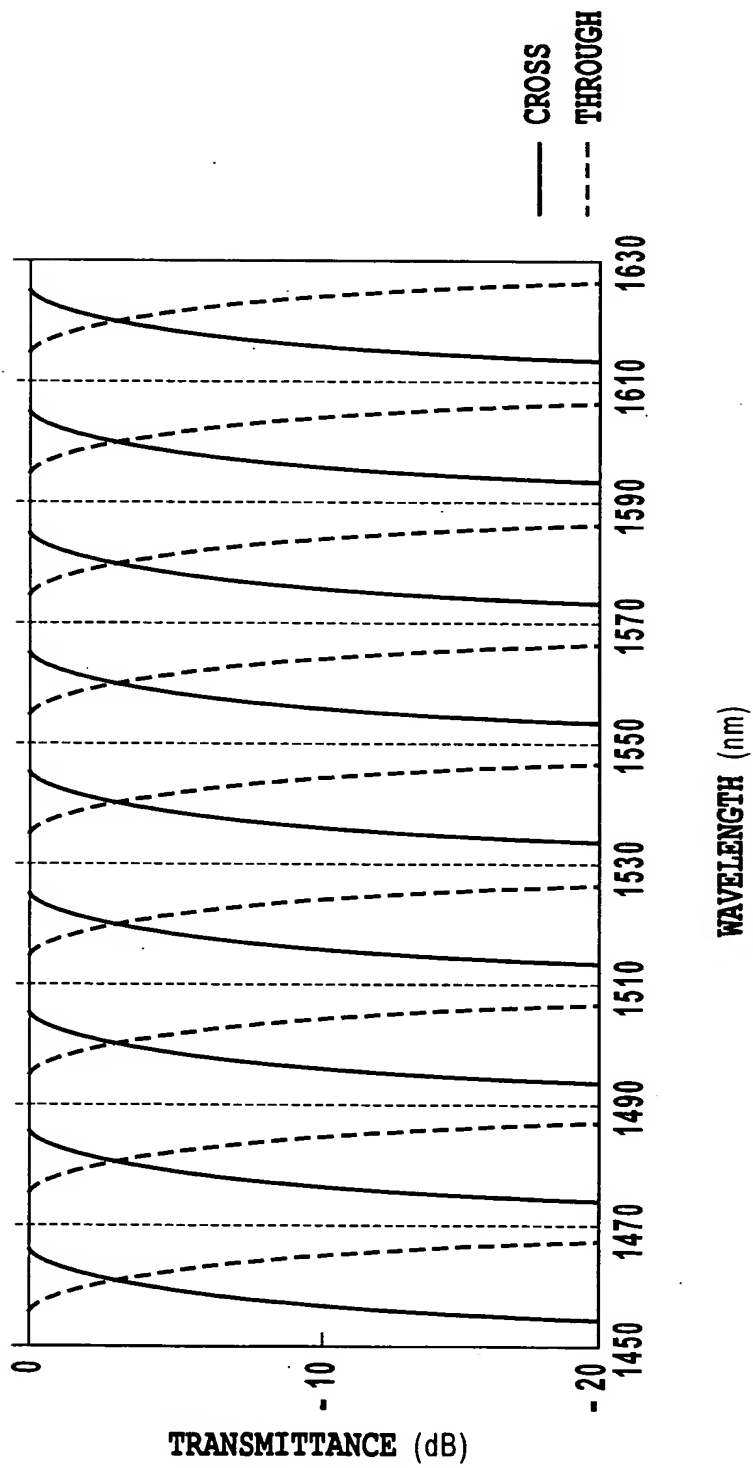


FIG.20

21/80

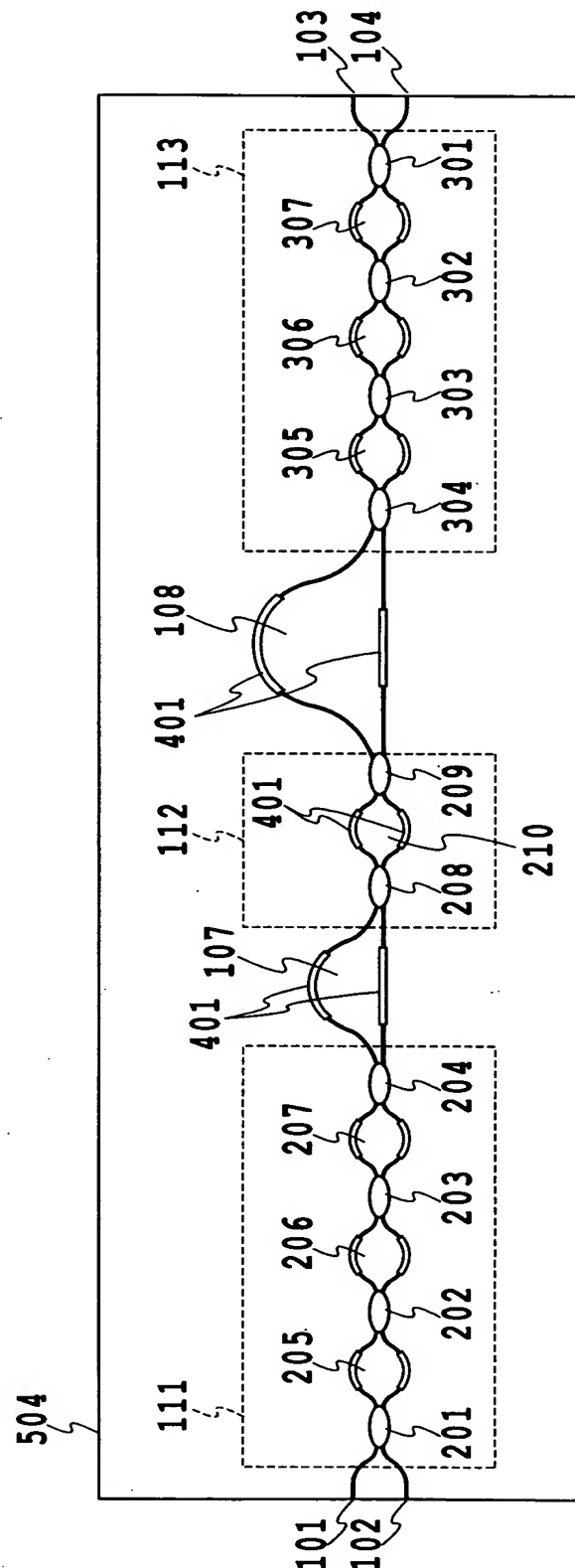


FIG.21

22/80

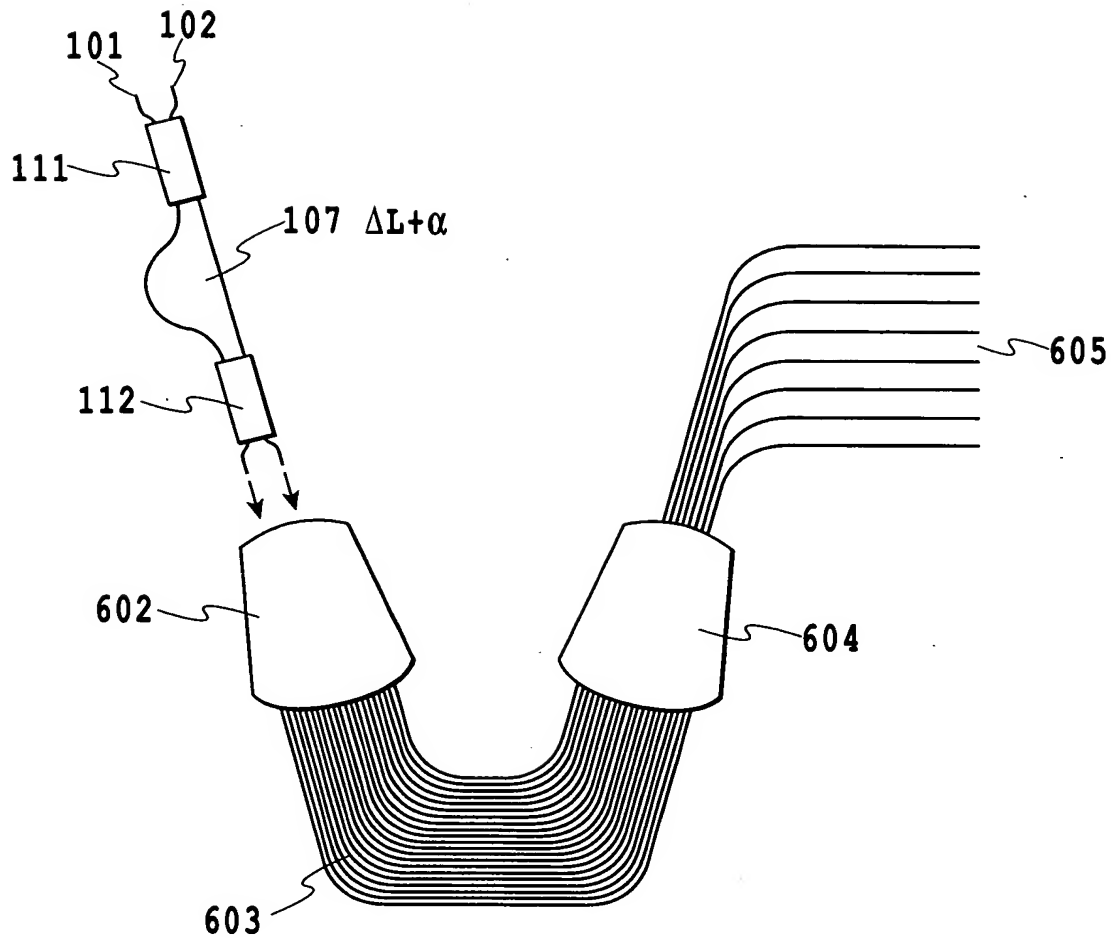


FIG.22

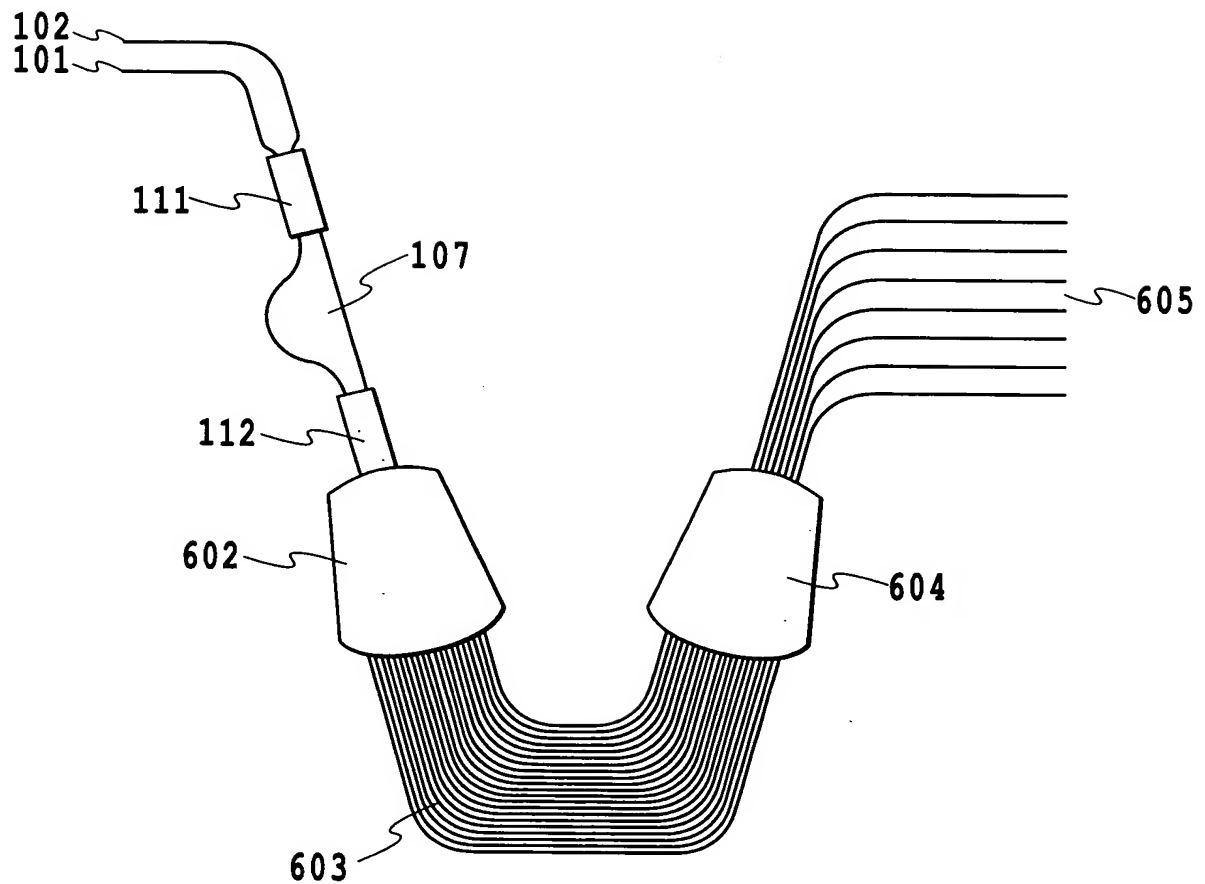


FIG.23

24/80

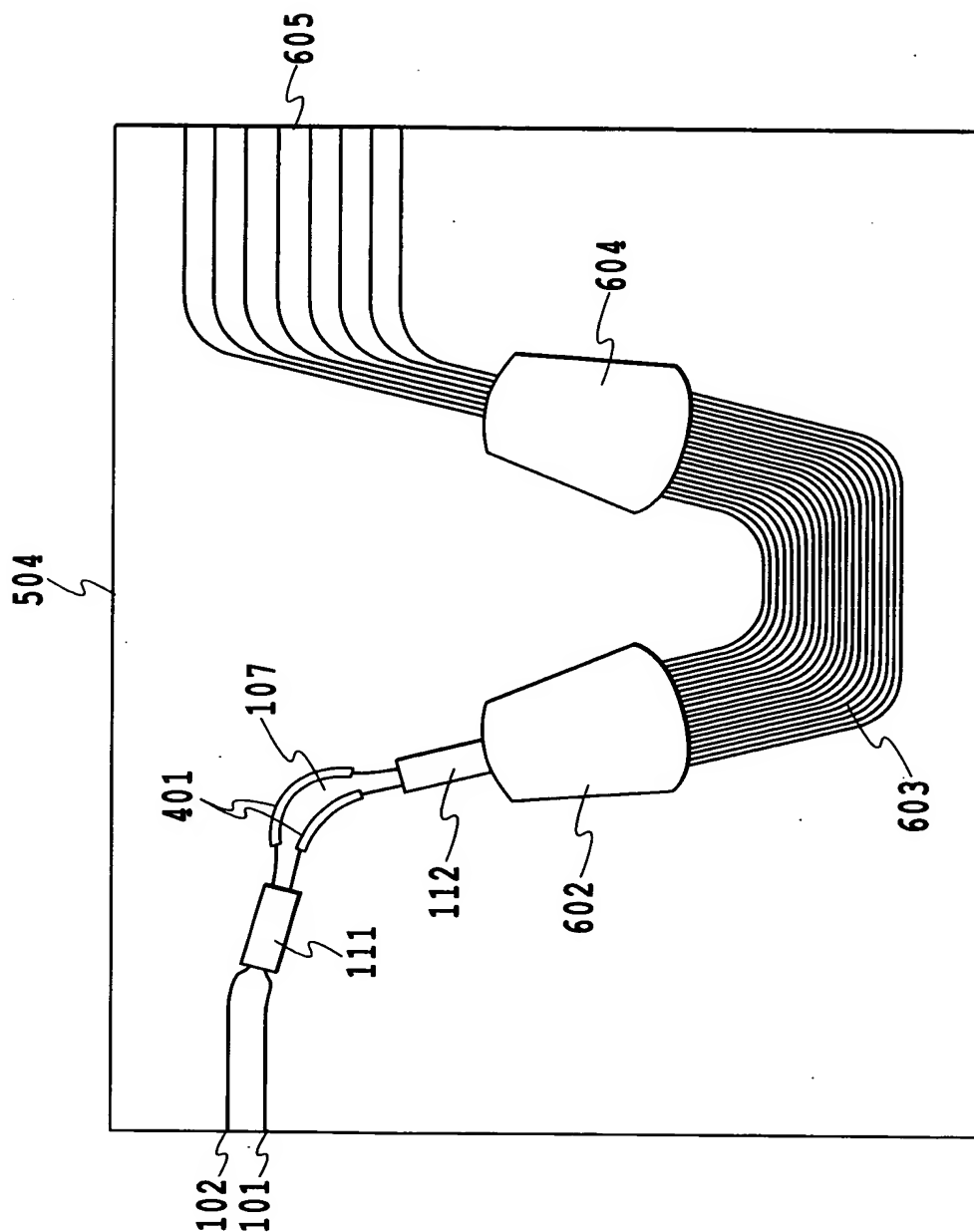


FIG.24

Title: OPTICAL MULTI/DEMULTIPLEXING CIRCUIT
EQUIPPED WITH PHASE GENERATING DEVICE

Inventors: Takayuki Mizuno et al.

Docket No.: 14321.60

25/80

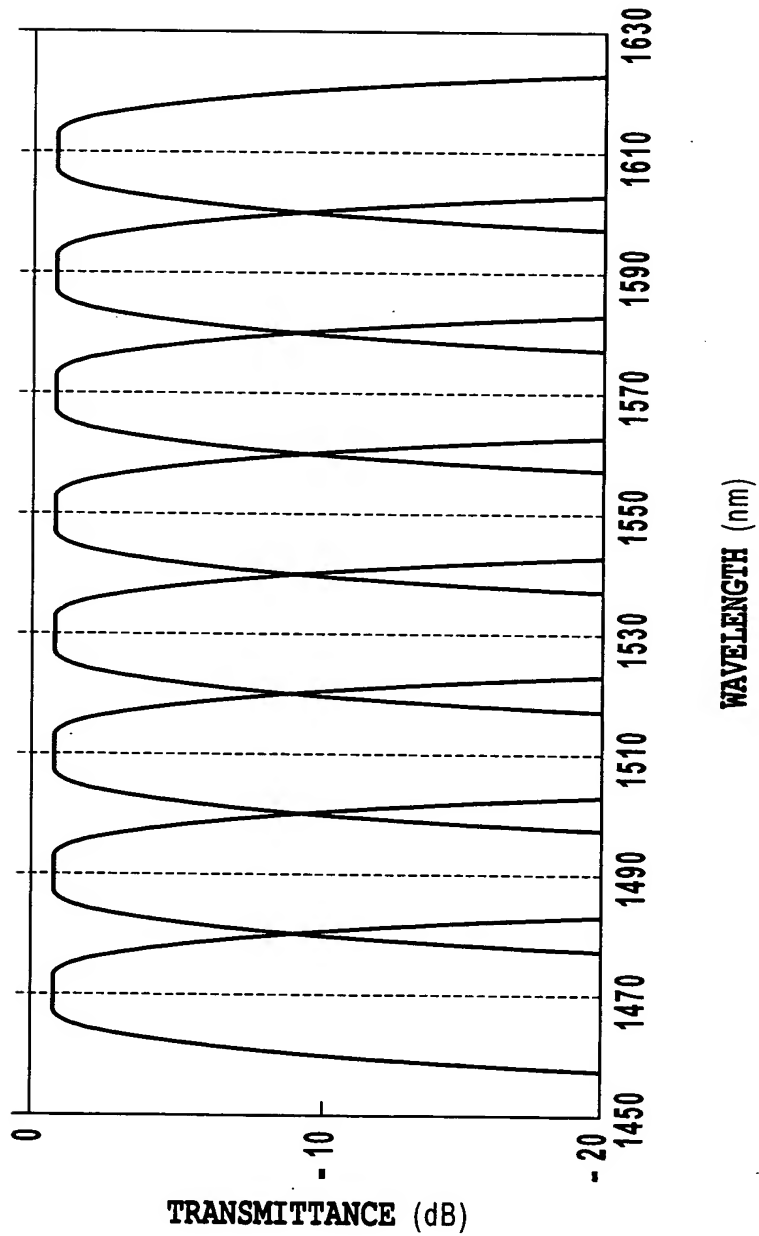


FIG.25

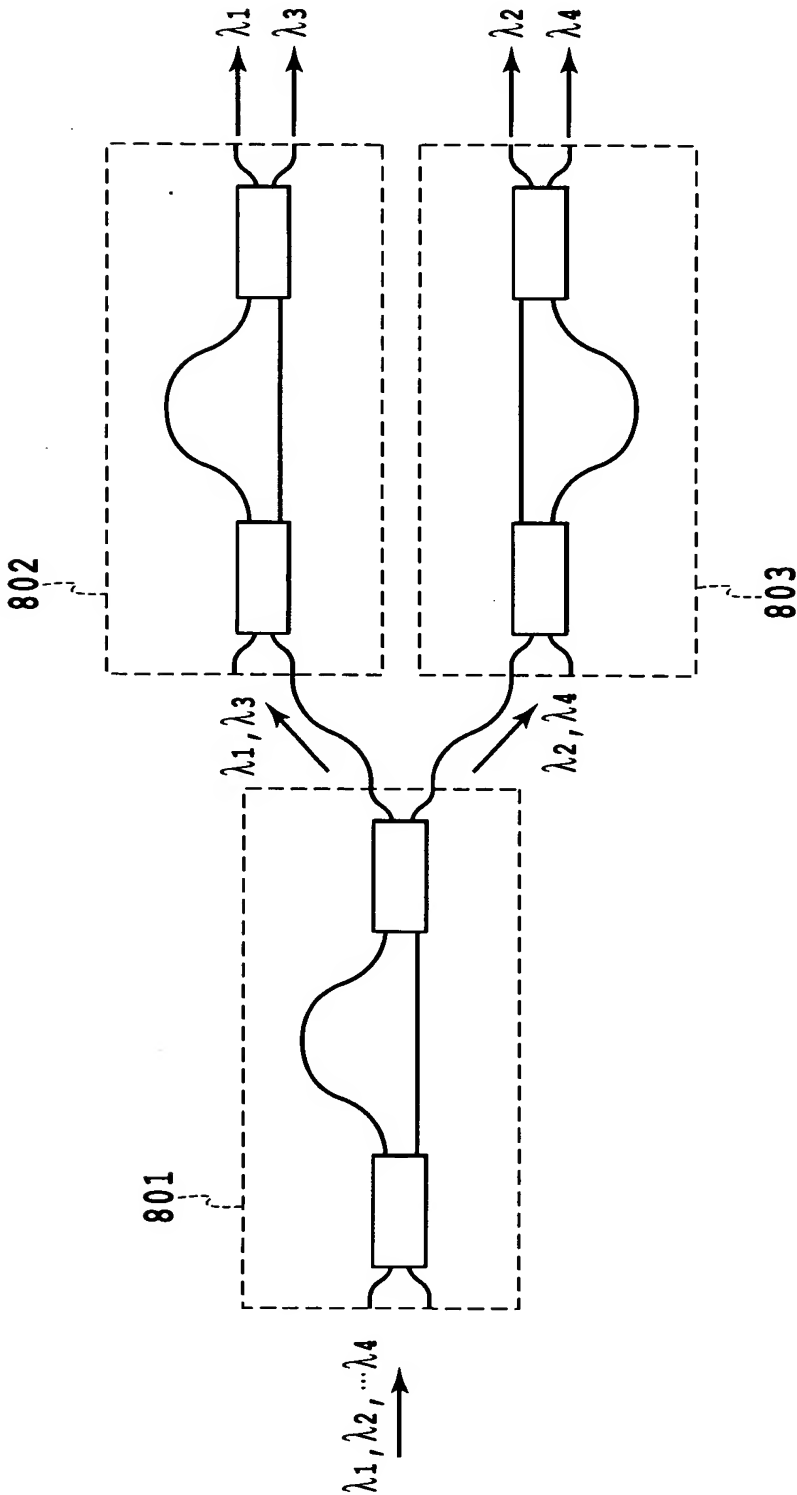


FIG. 26

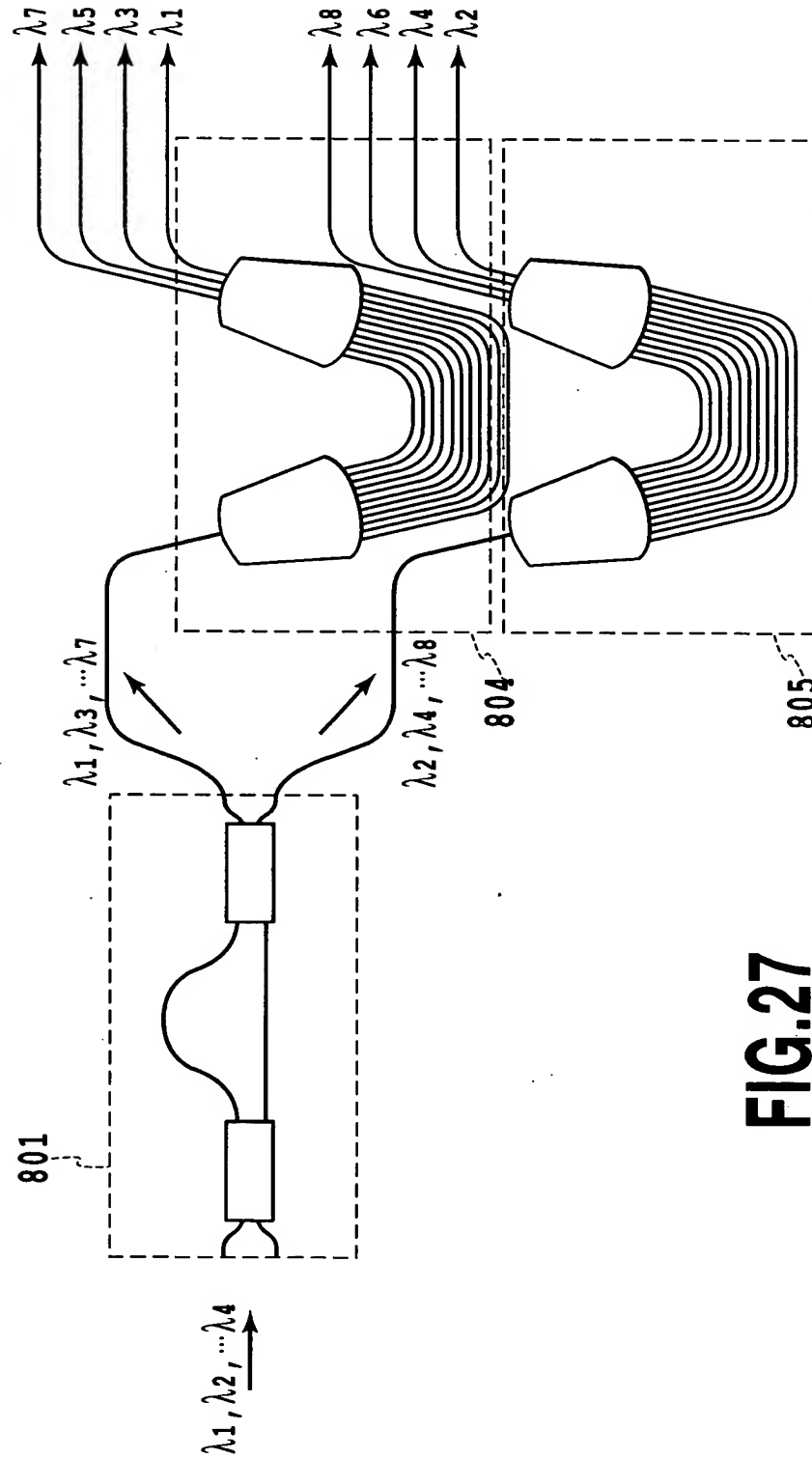


FIG.27

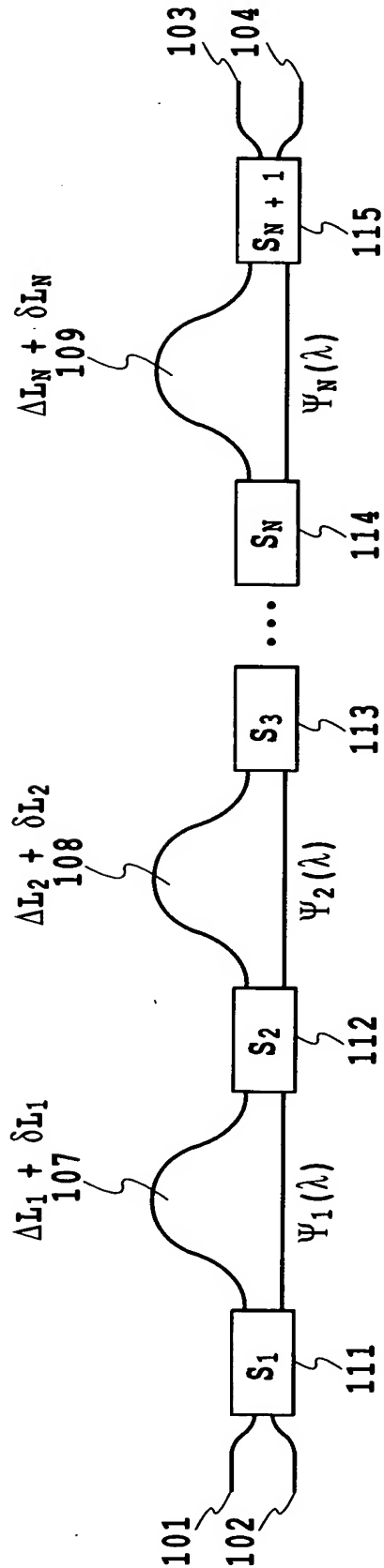


FIG. 28

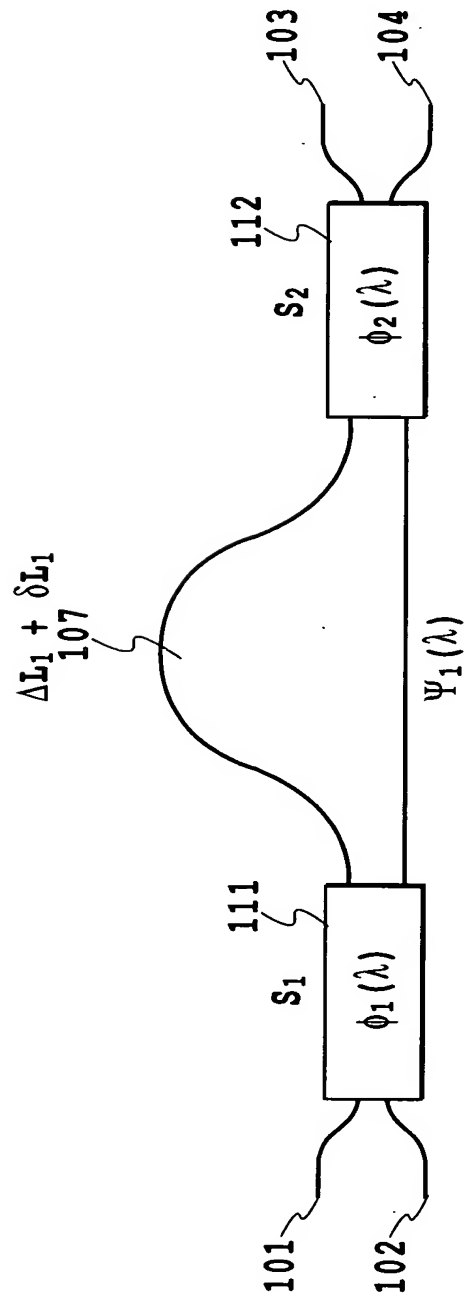


FIG.29

30/80

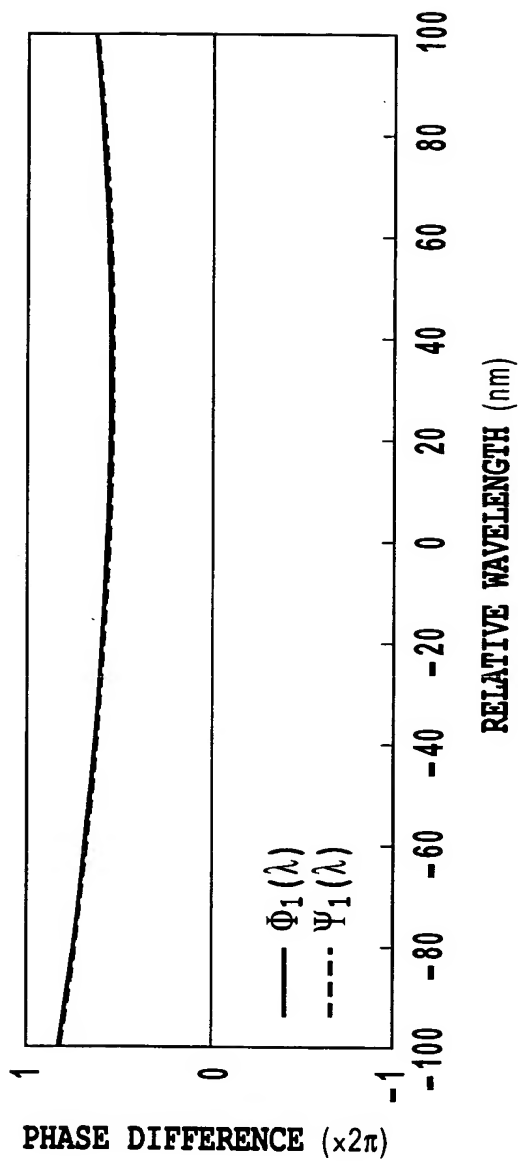


FIG.30A

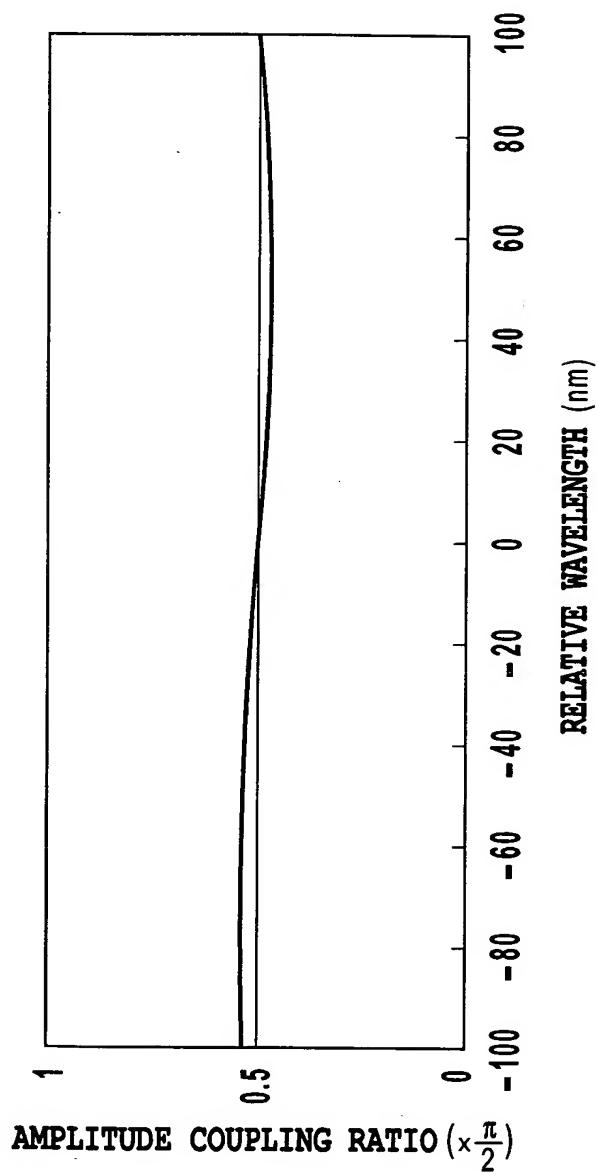


FIG.30B

31/80

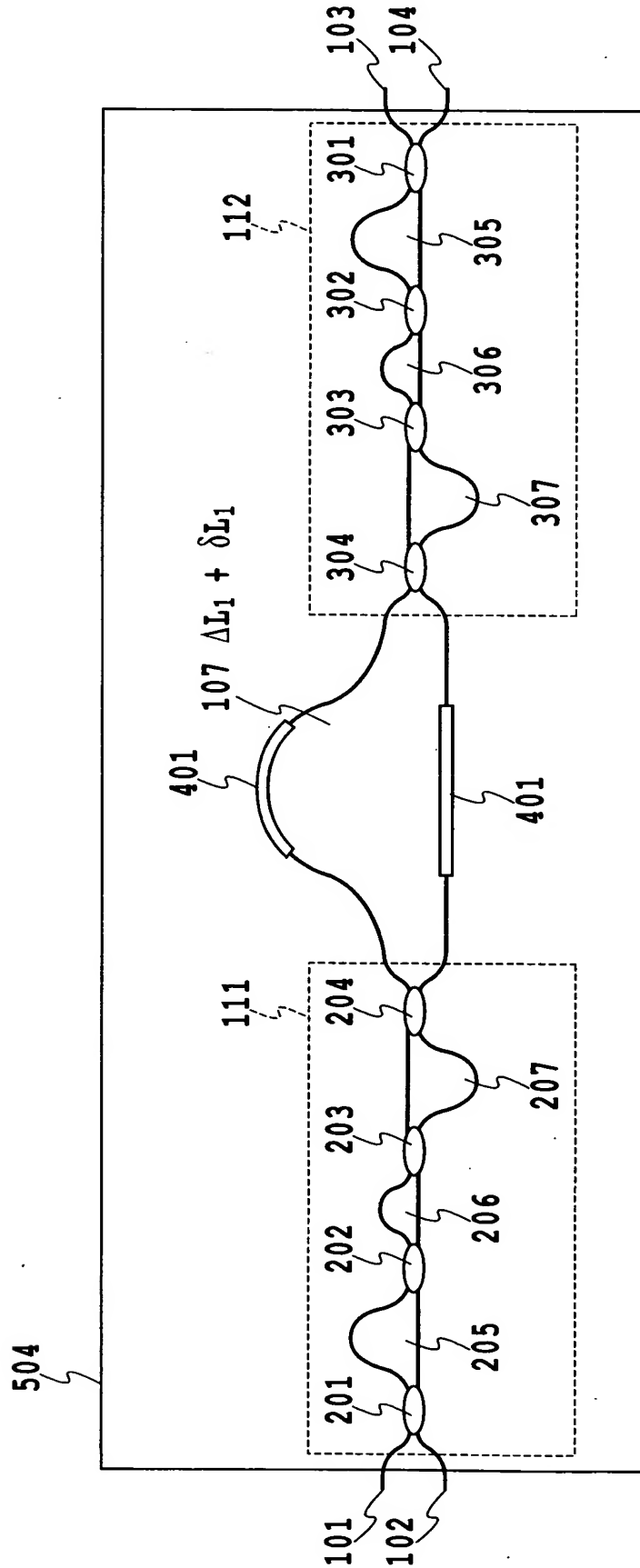


FIG.31

32/80

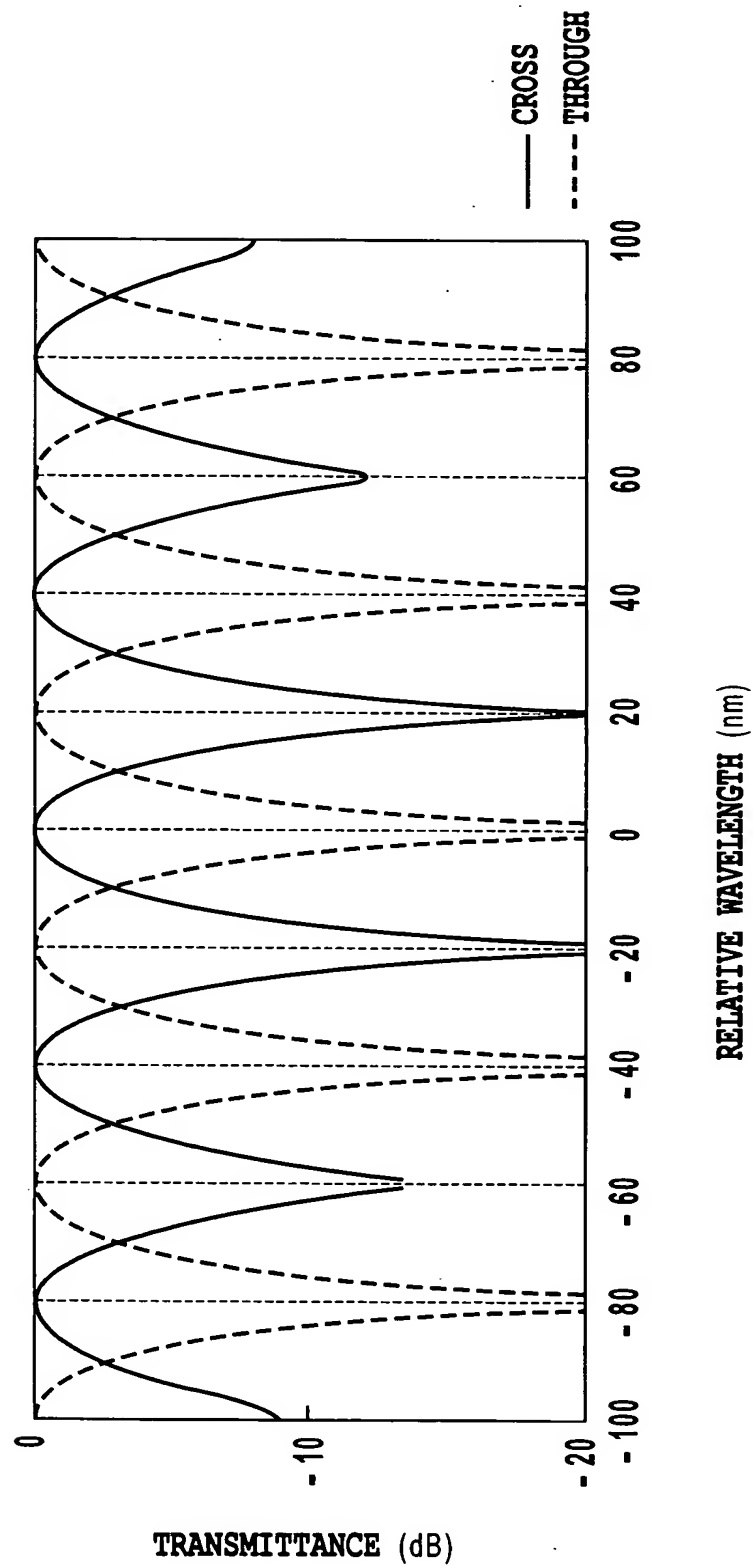


FIG.32

33/80

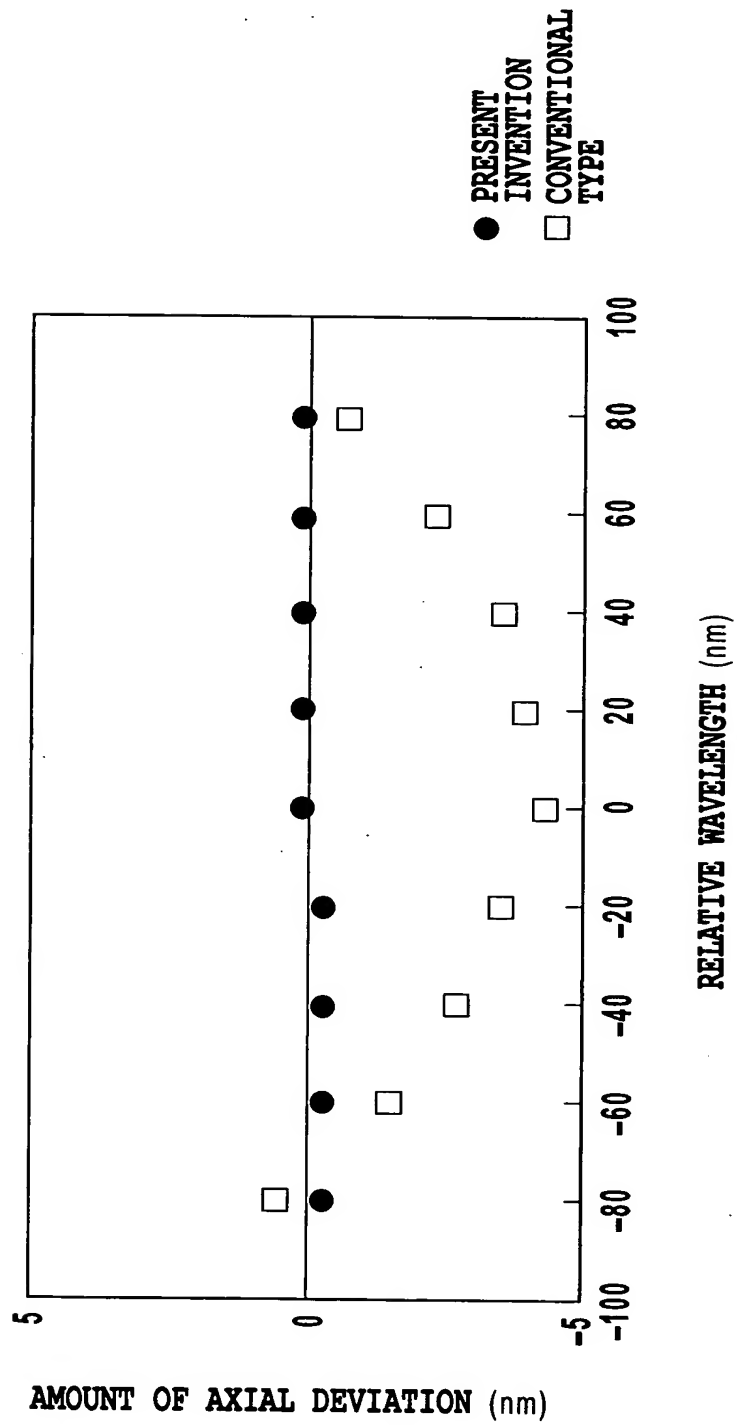


FIG.33

34/80

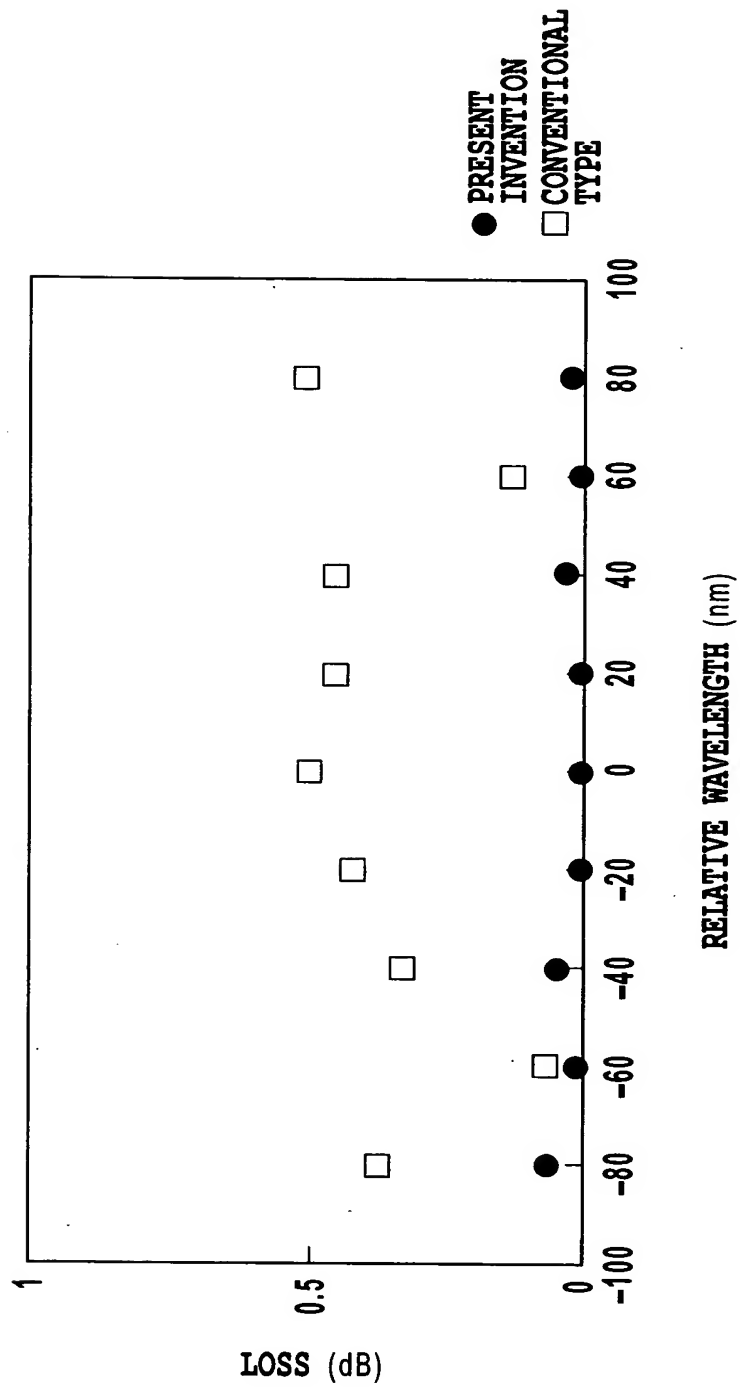


FIG.34

35/80

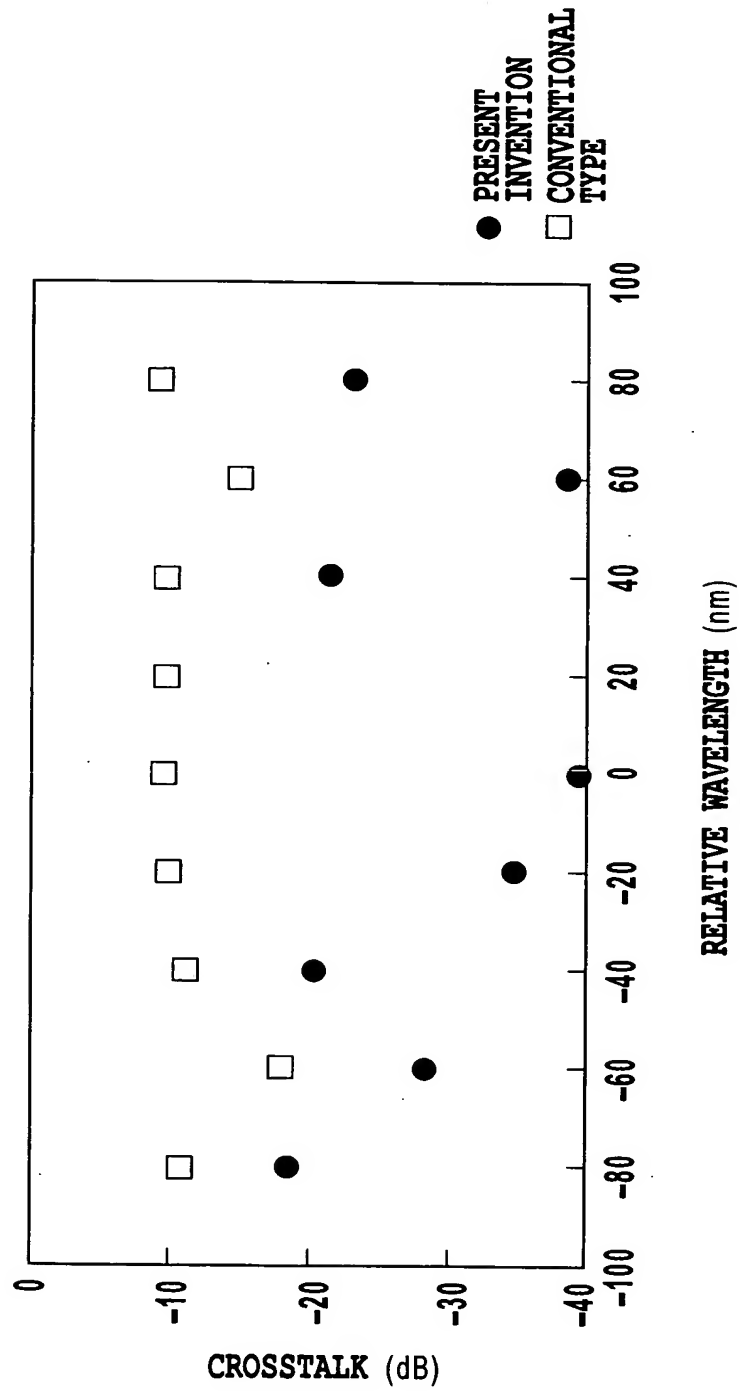


FIG.35

36/80

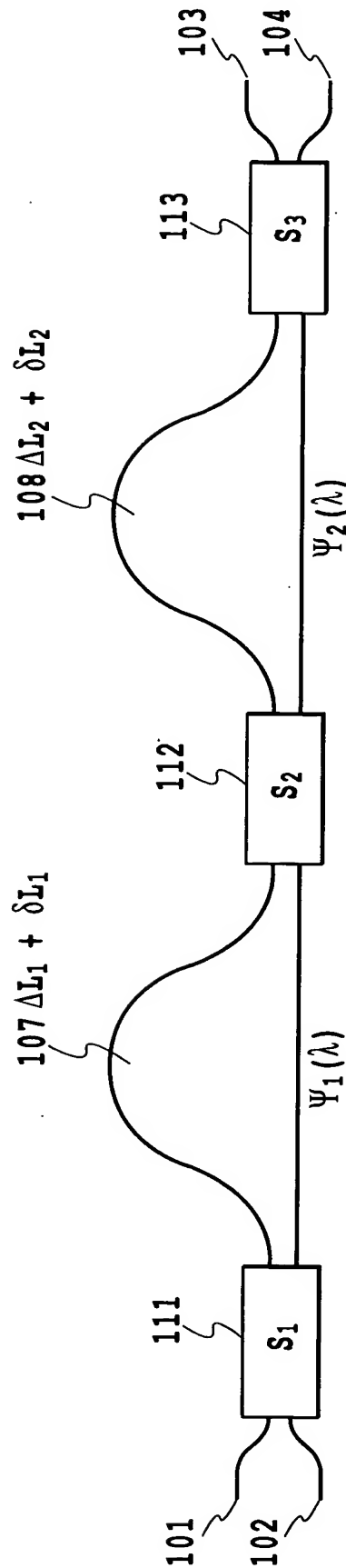


FIG.36

37/80

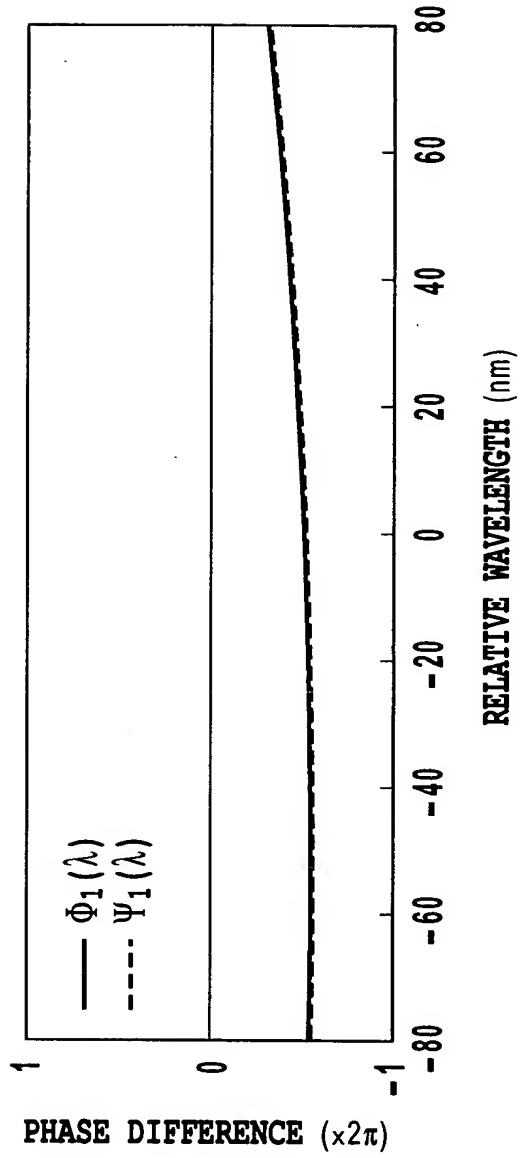


FIG.37A

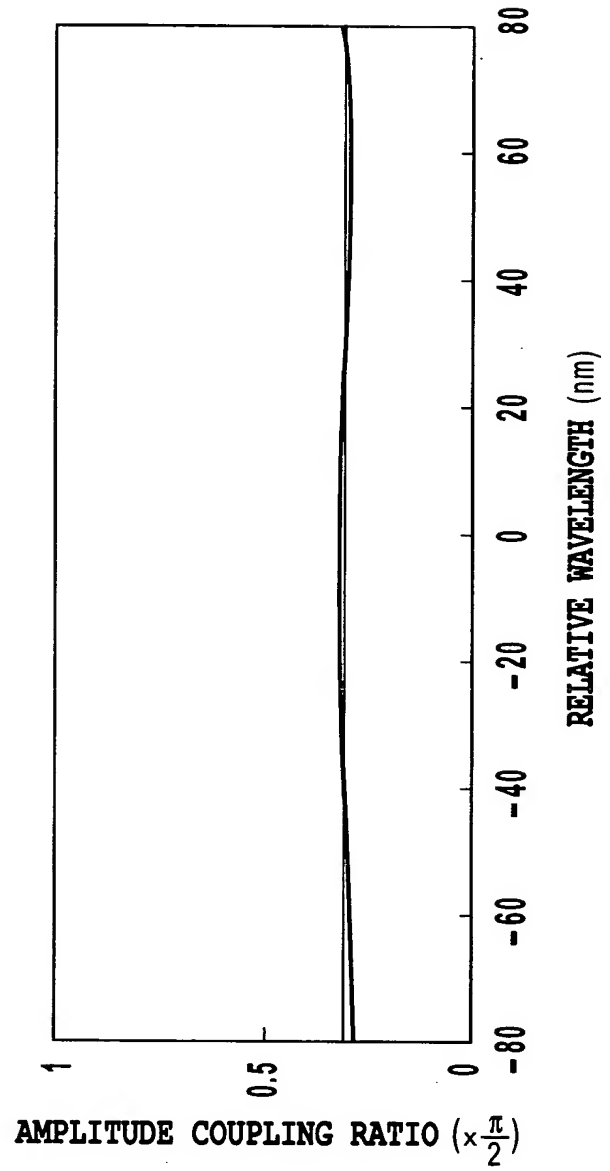


FIG.37B

38/80

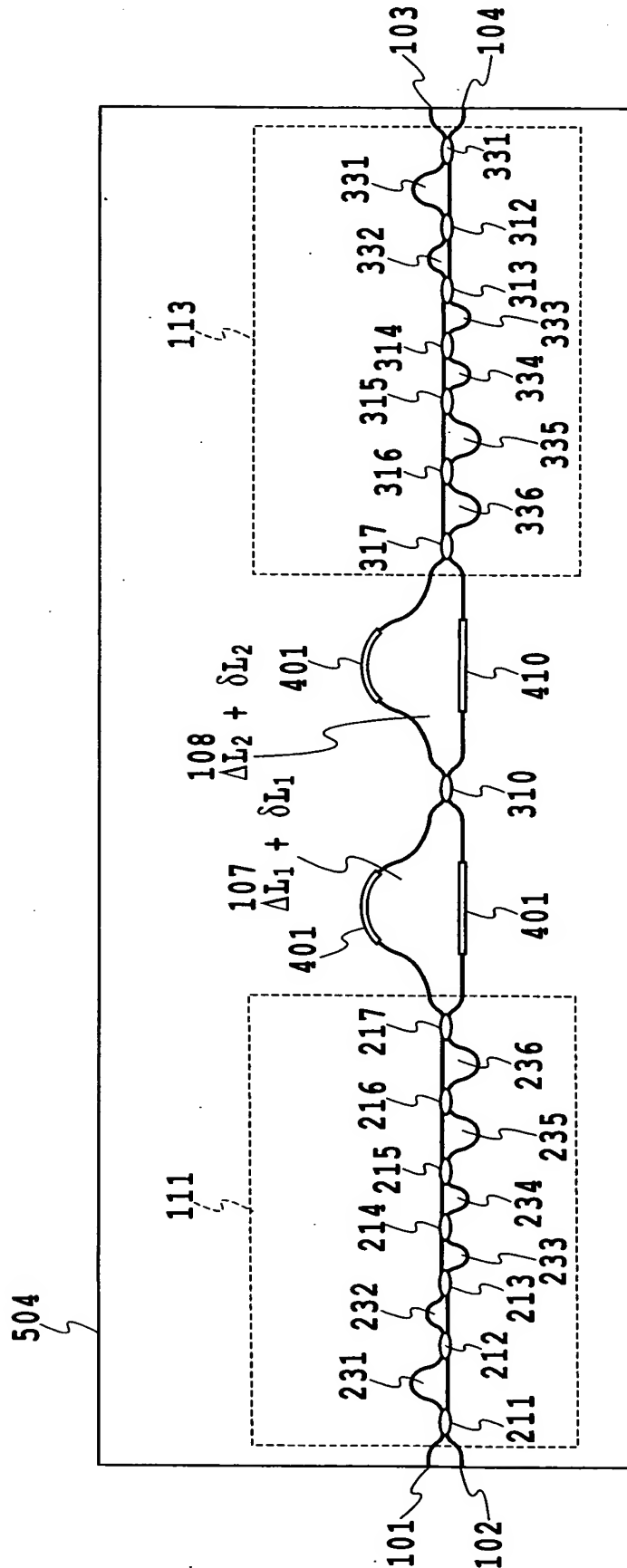


FIG.38

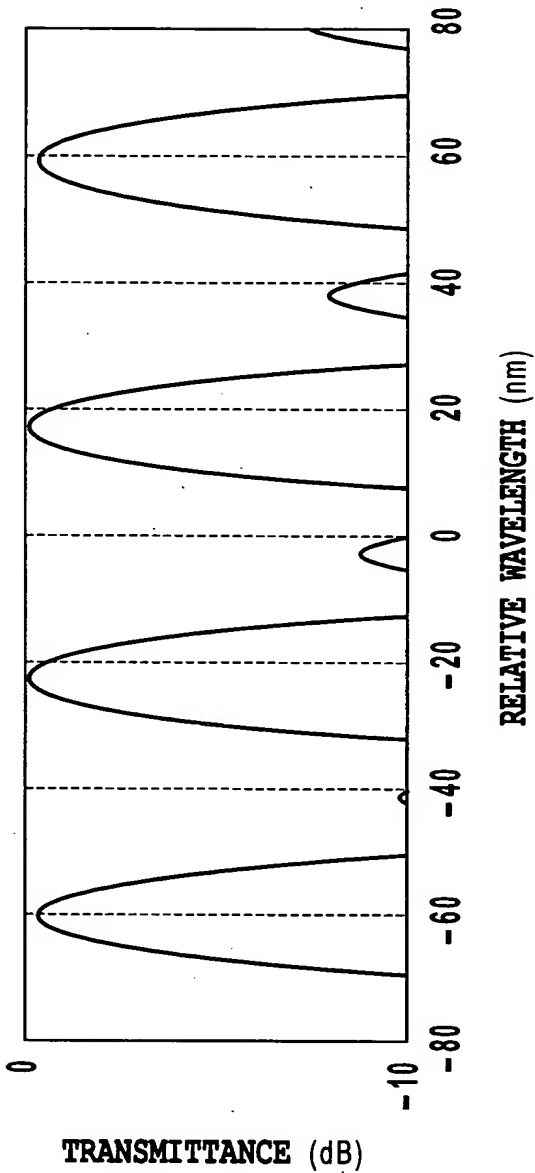


FIG. 39A

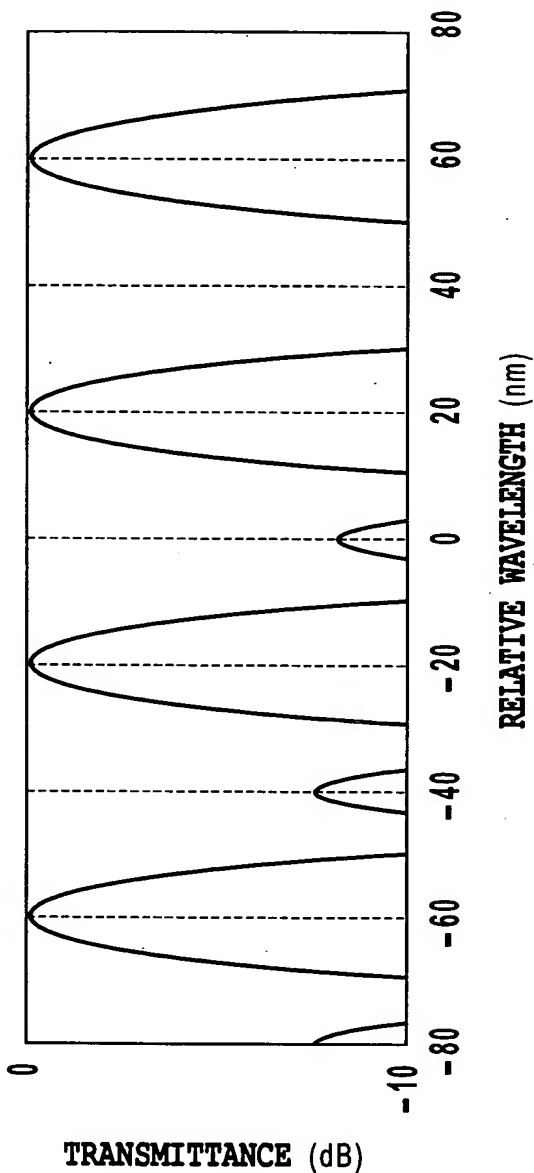


FIG. 39B

40/80

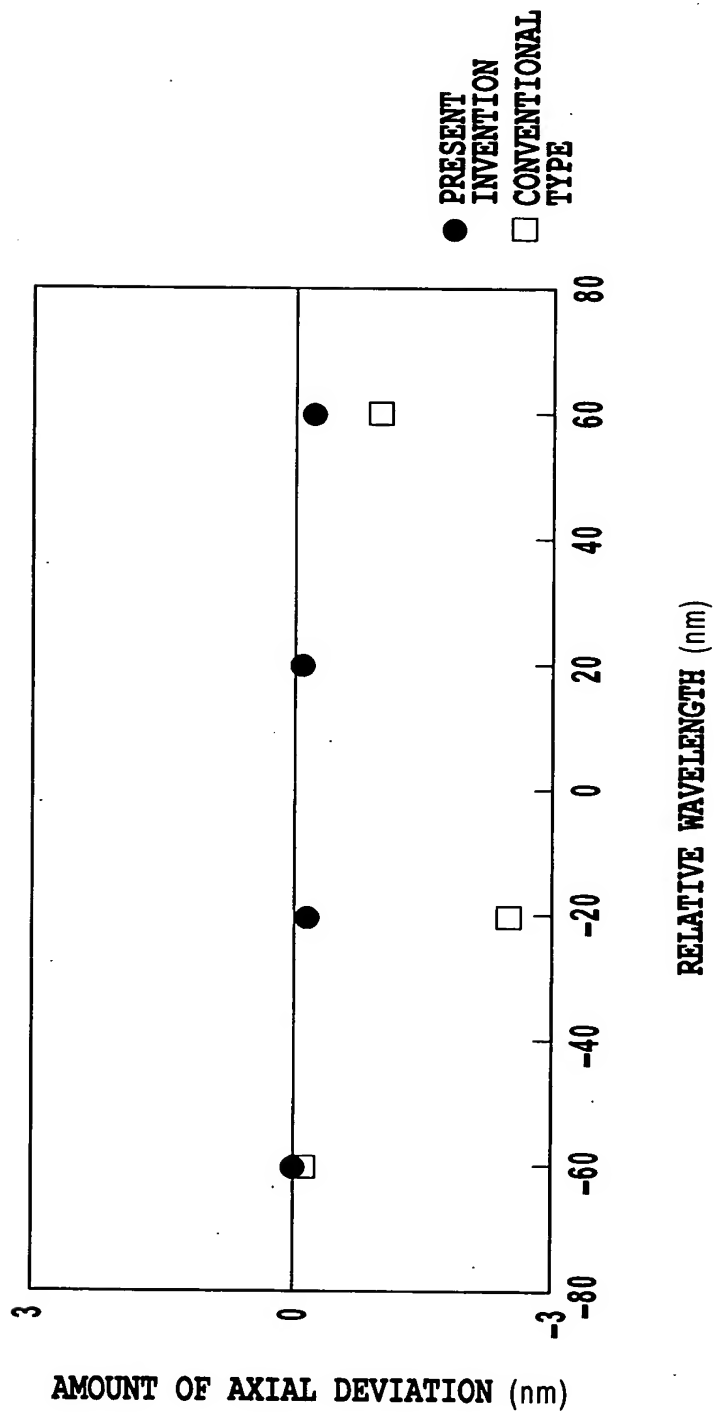


FIG.40

41/80

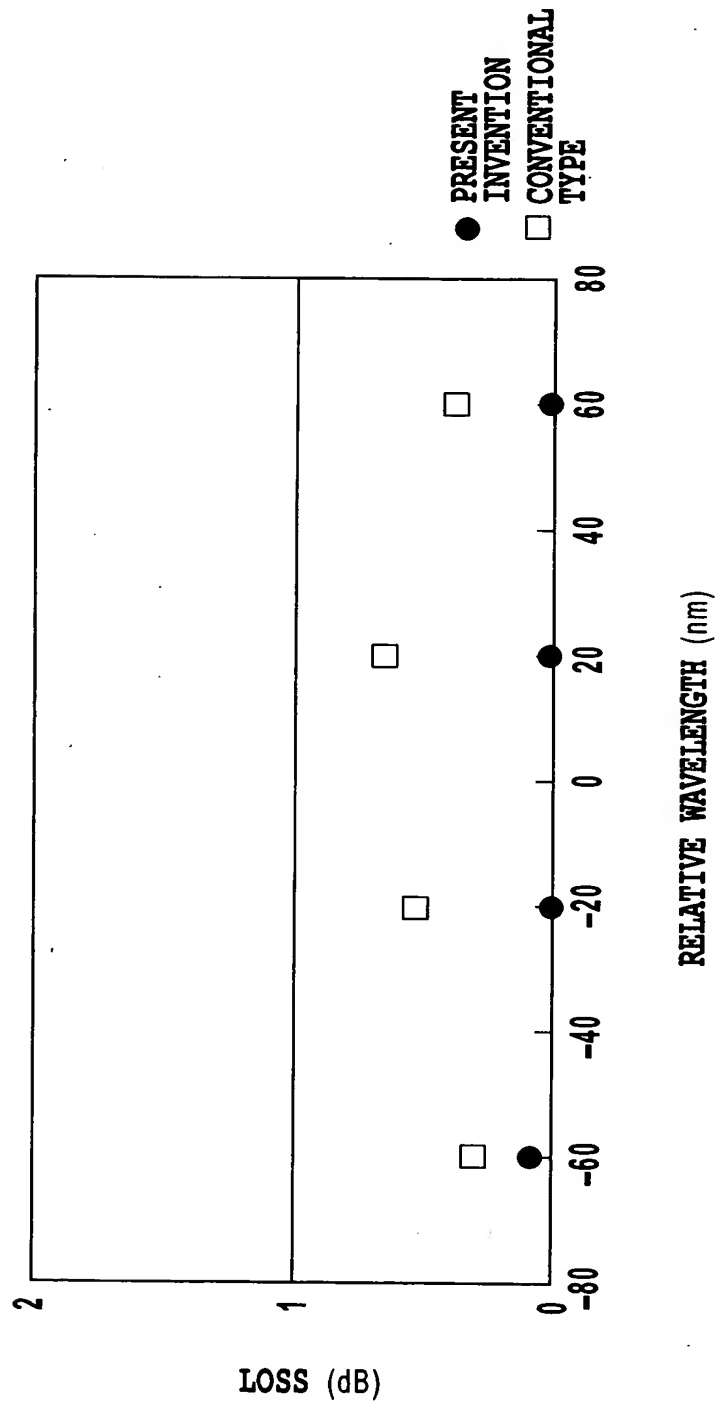


FIG.41

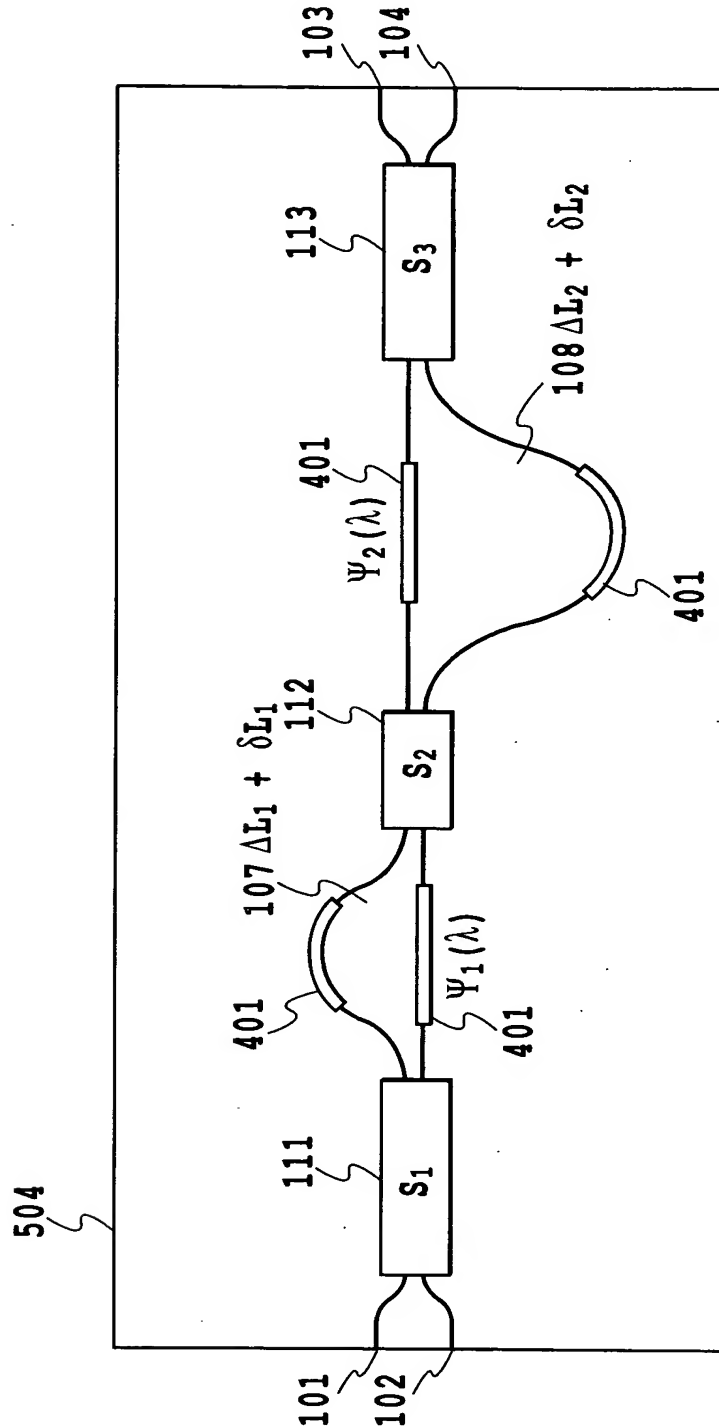


FIG.42

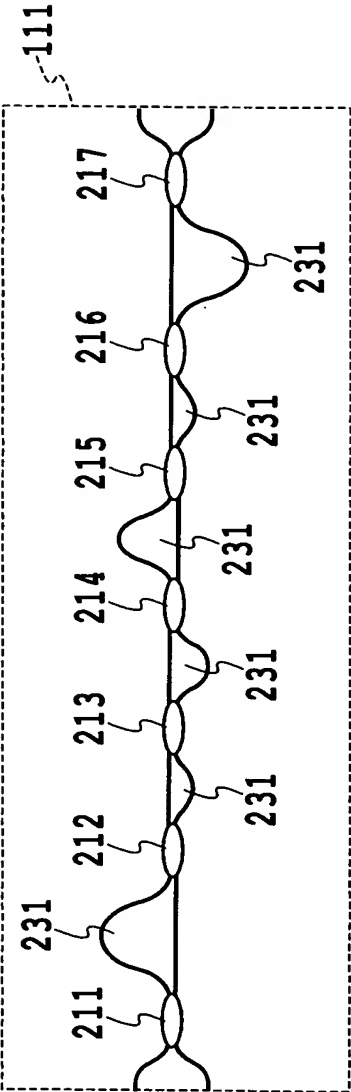


FIG. 43A

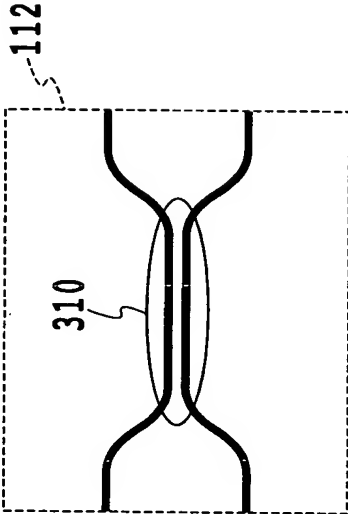


FIG. 43B

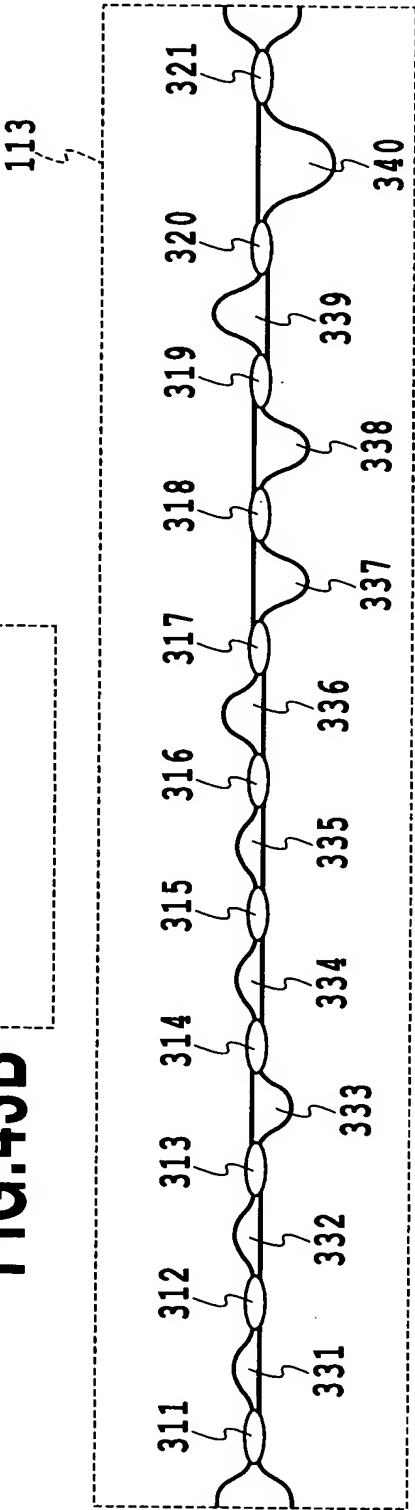


FIG. 43C

44/80

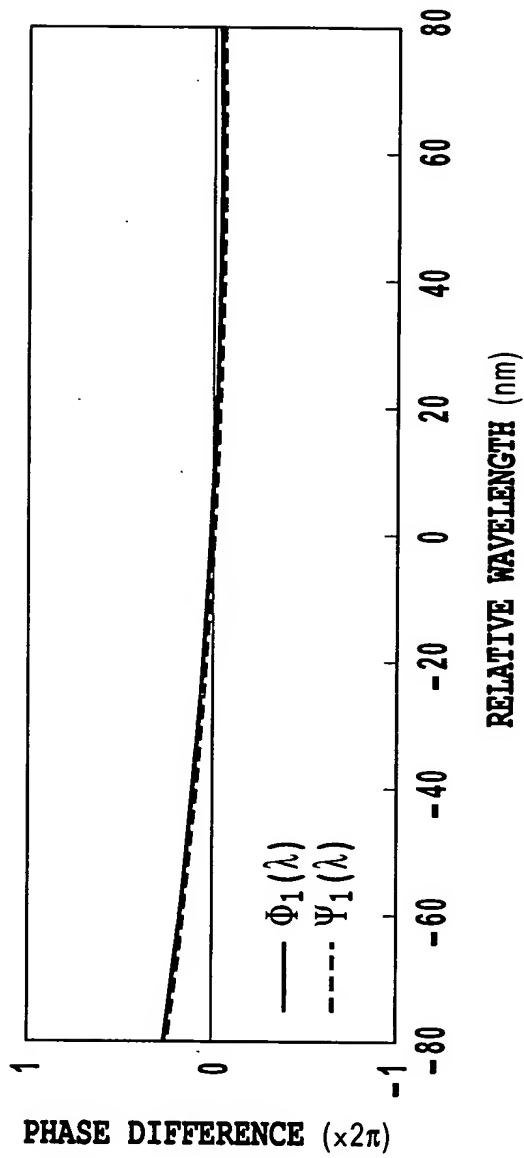


FIG. 44A

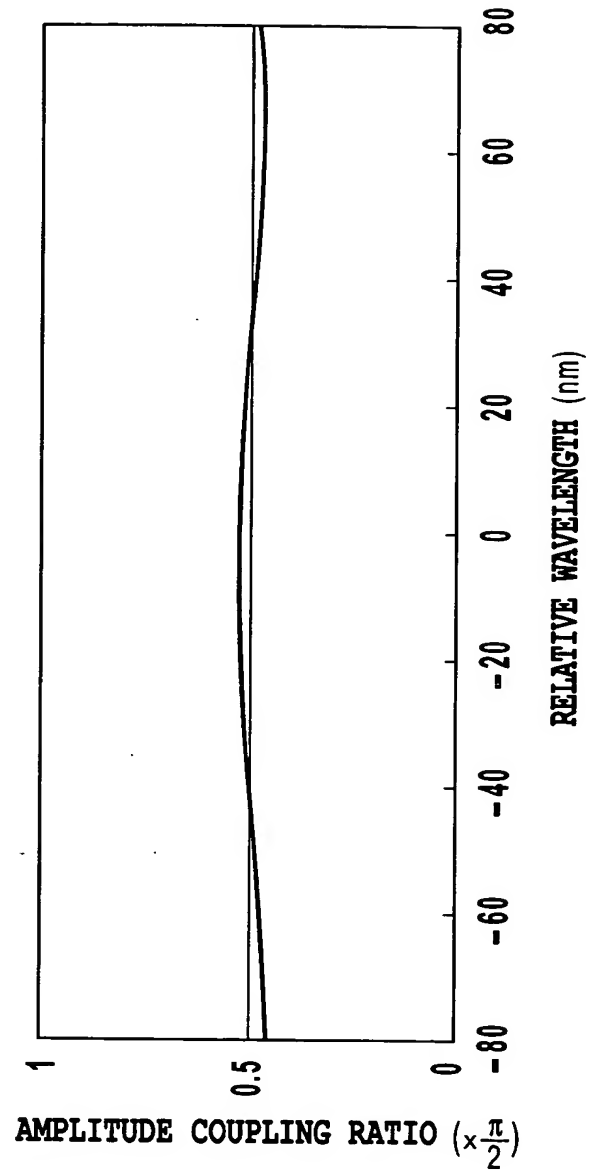


FIG. 44B

45/80

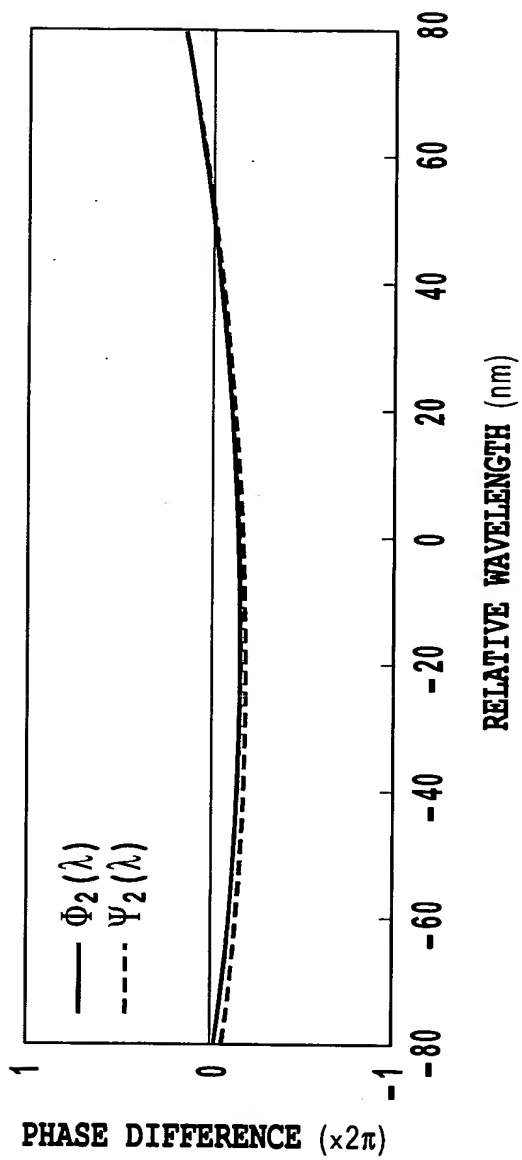


FIG. 45A

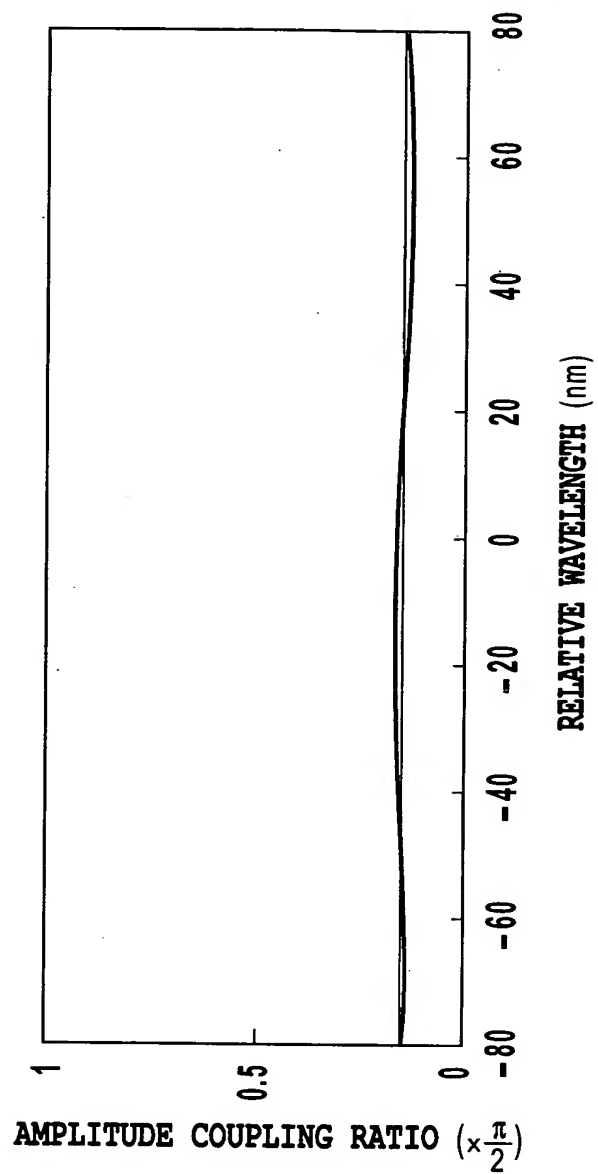


FIG. 45B

46/80

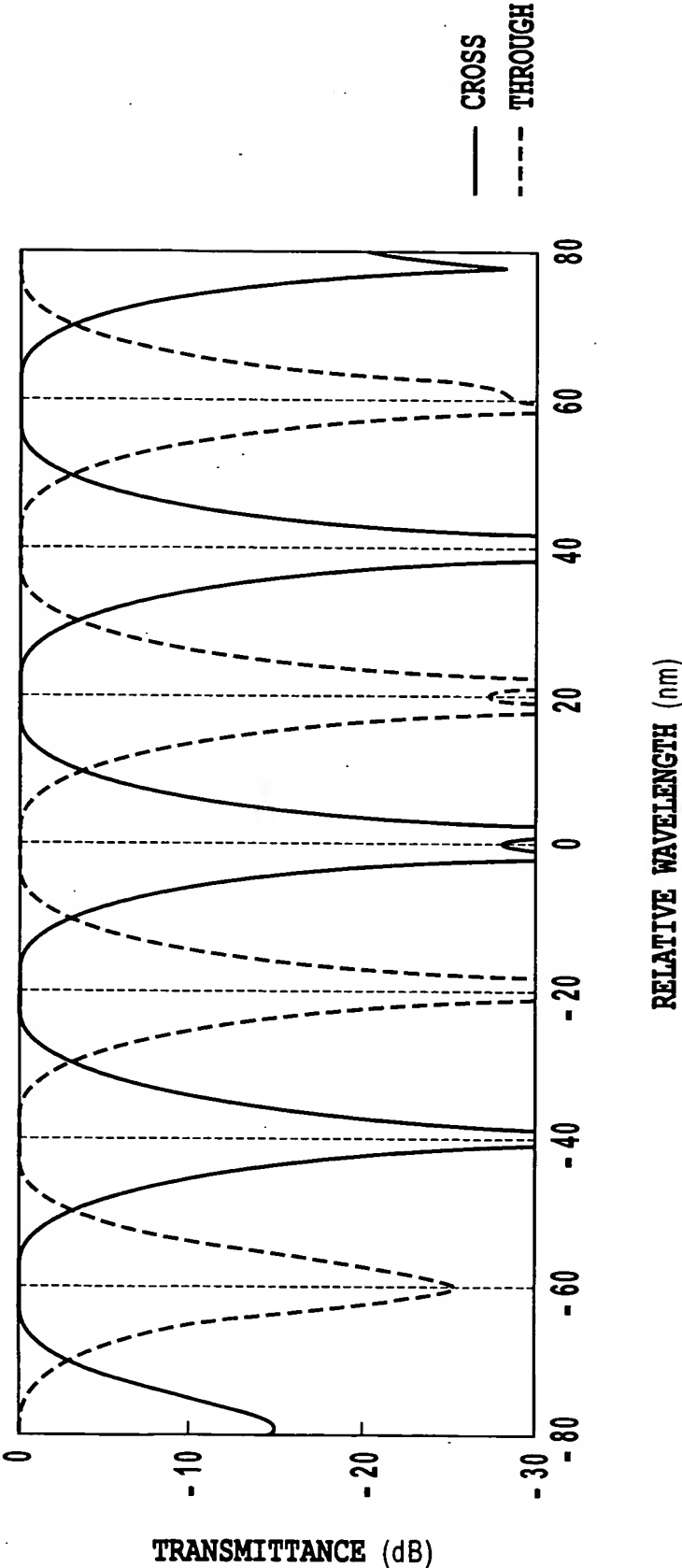


FIG.46

47/80

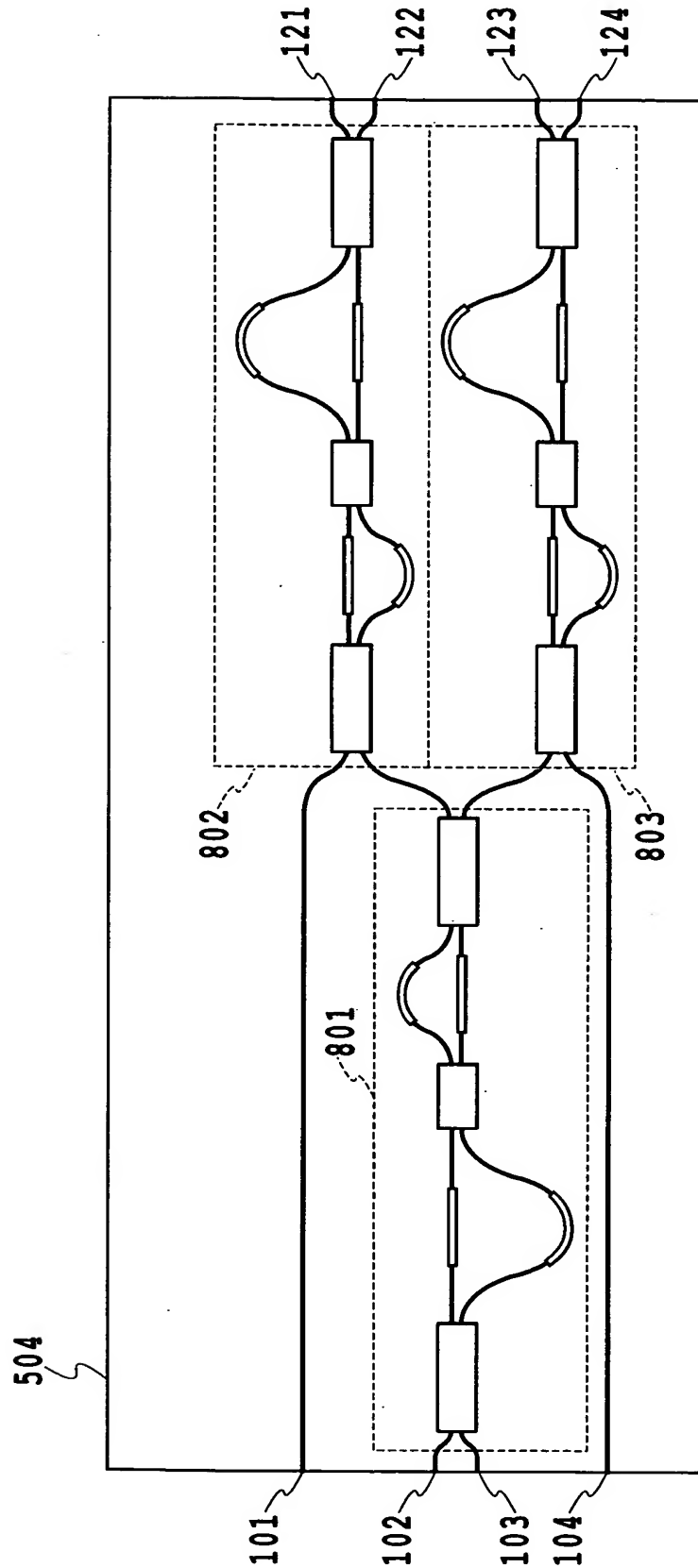


FIG. 47

48/80

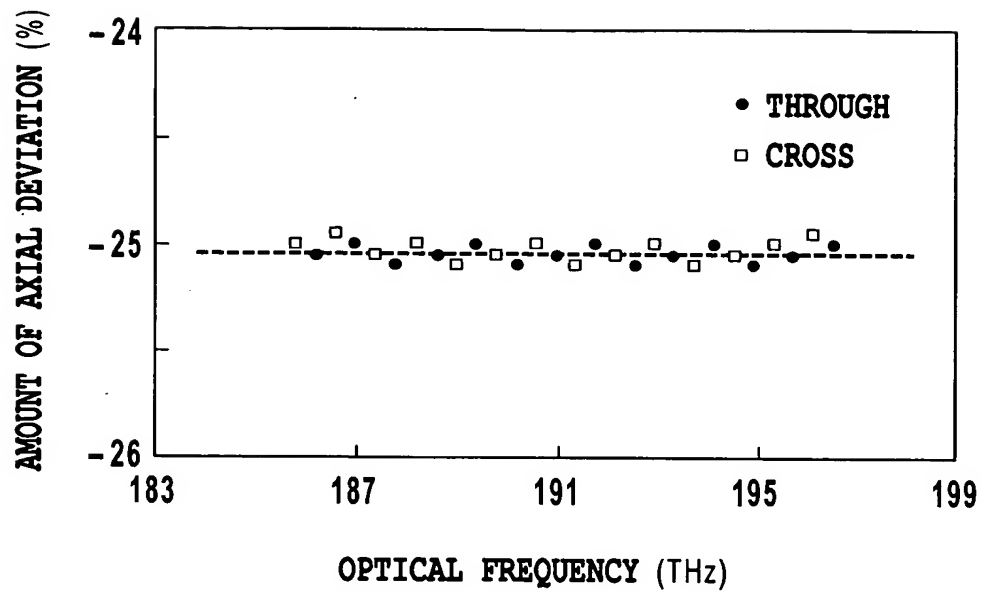


FIG. 48A

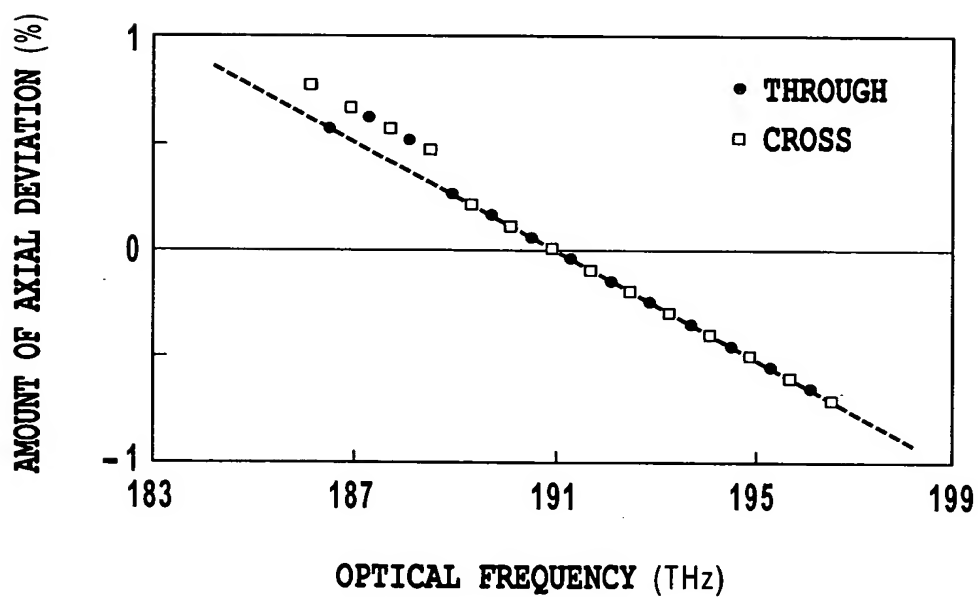


FIG. 48B

Title: OPTICAL MULTI/DEMULTIPLEXING CIRCUIT
EQUIPPED WITH PHASE GENERATING DEVICE

Inventors: Takayuki Mizuno et al.

Docket No.: 14321.60

49/80

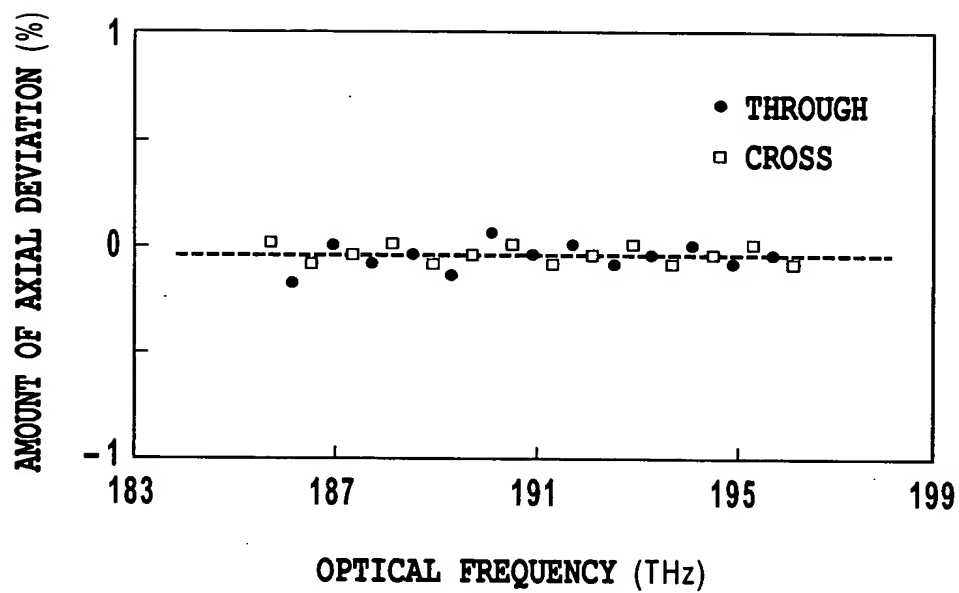


FIG.49

50/80

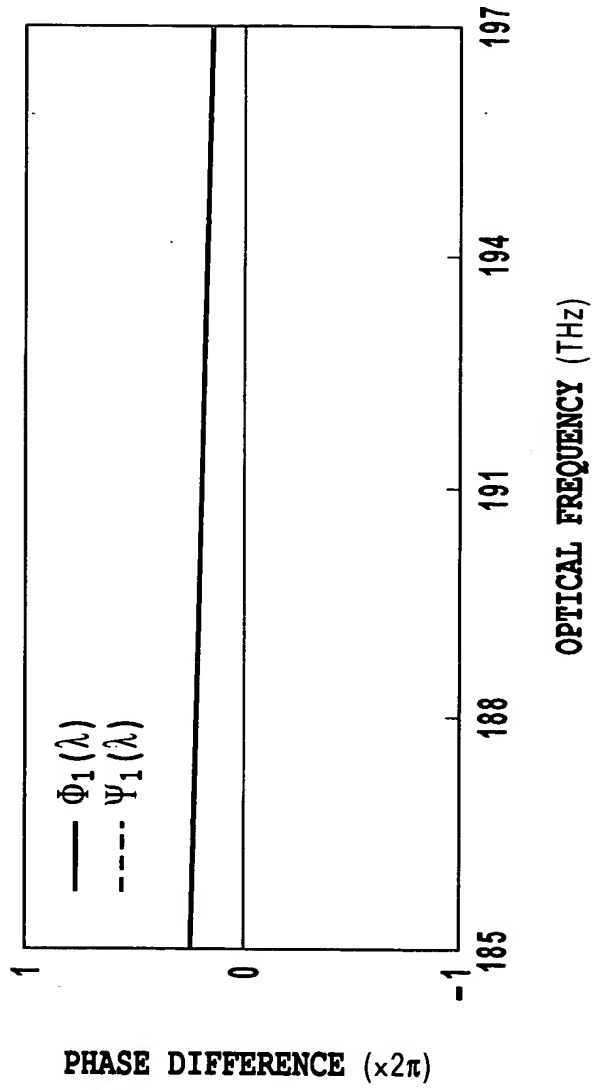


FIG. 50A

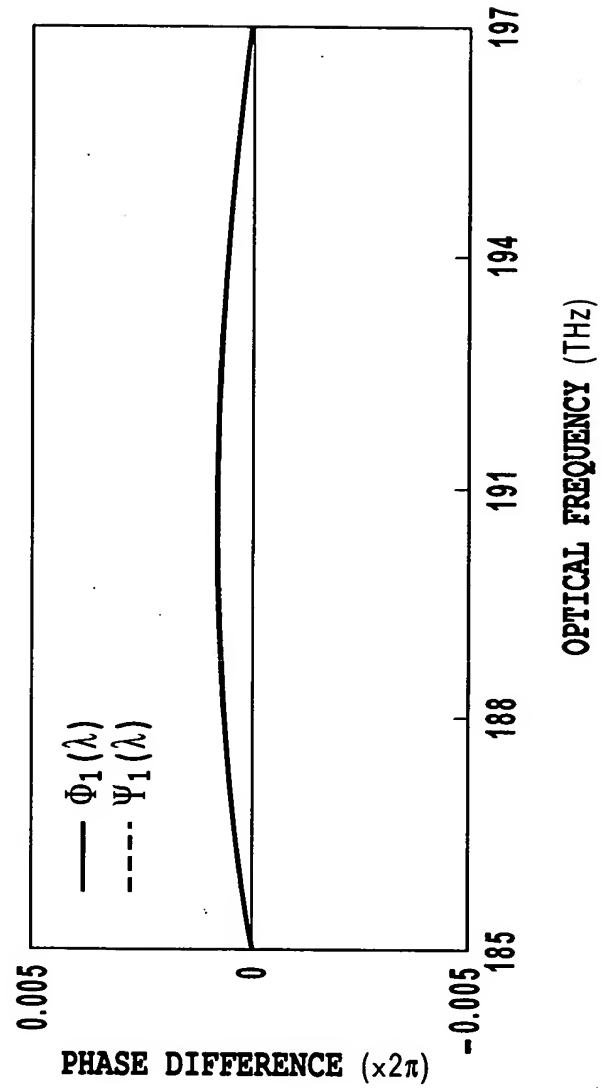


FIG. 50B

51/80

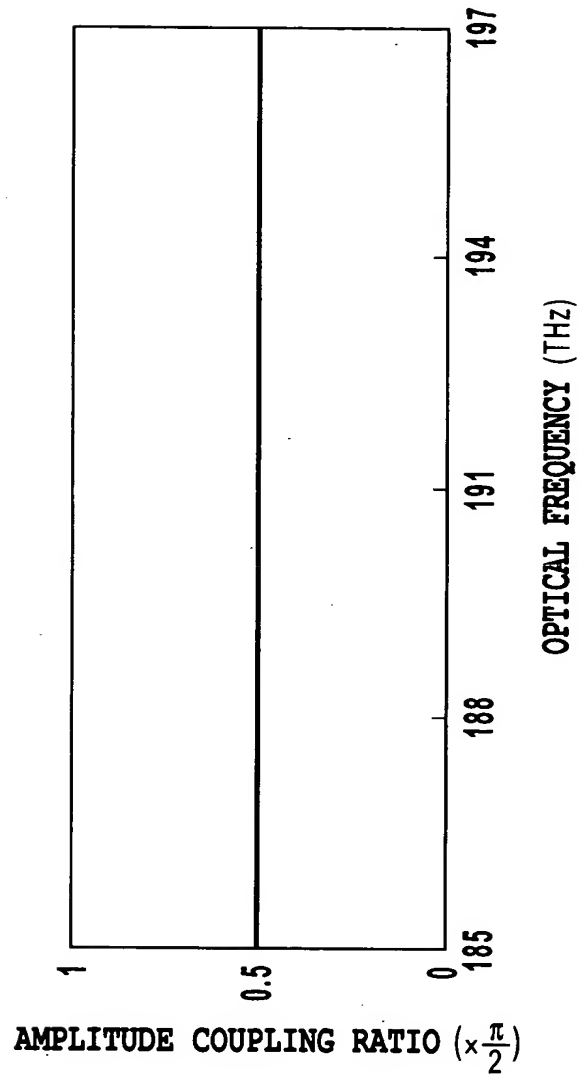


FIG.51

52/80

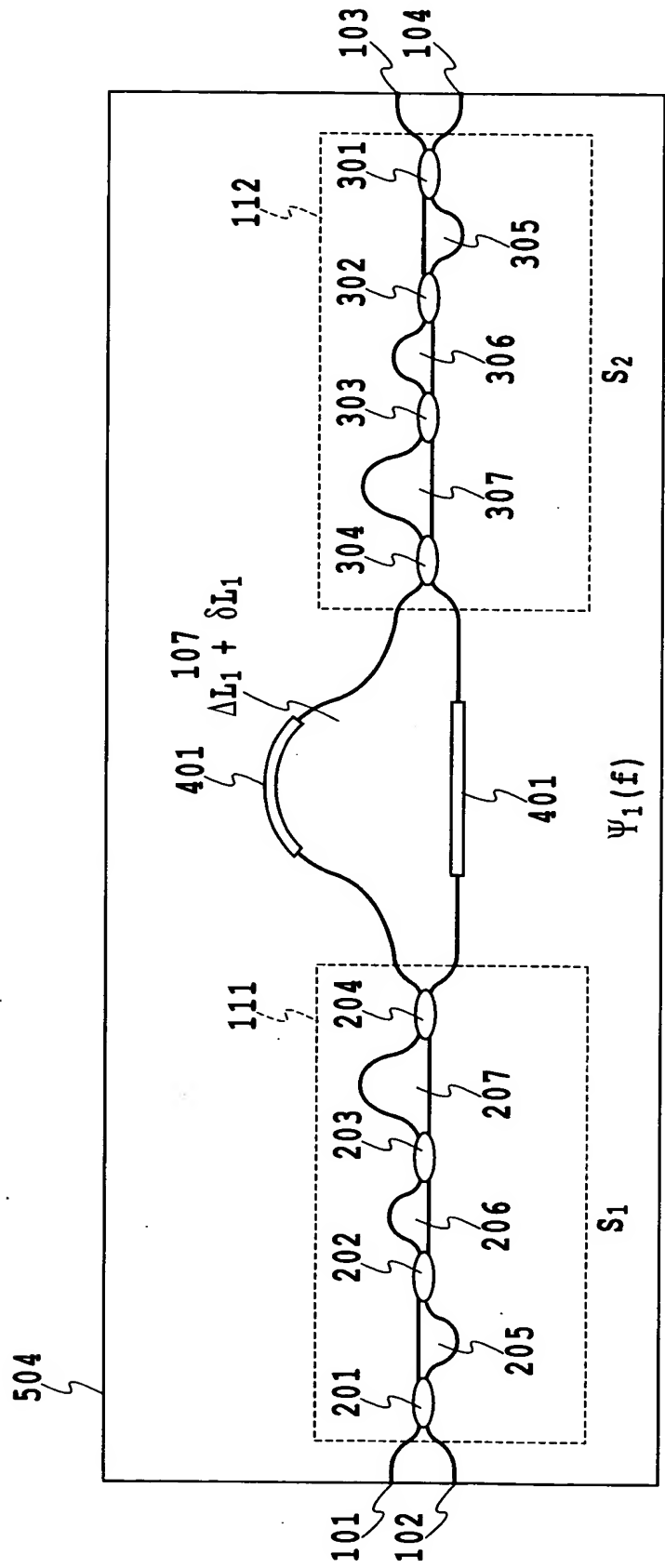


FIG.52

53/80

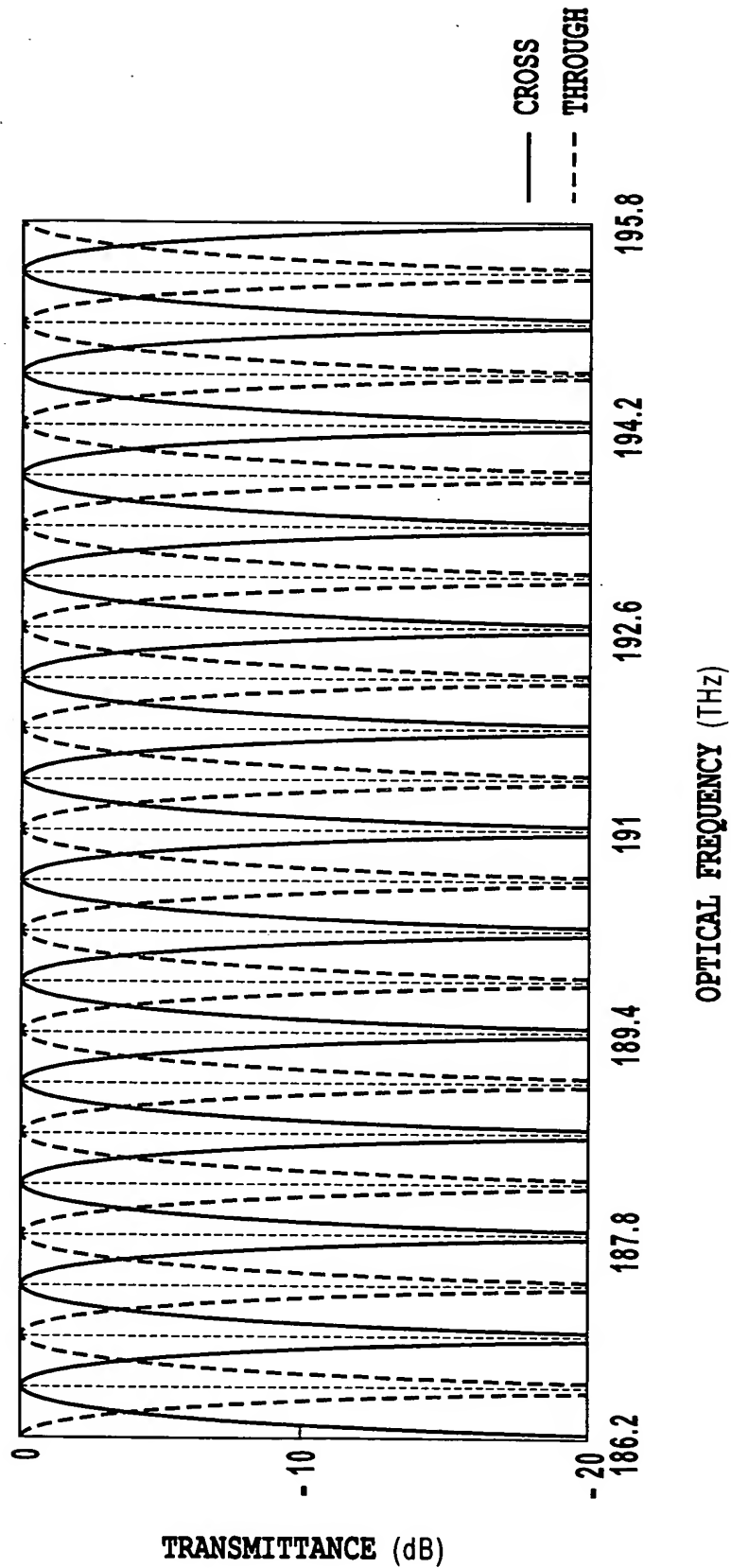


FIG.53

Title: OPTICAL MULTI/DEMULTIPLEXING CIRCUIT
EQUIPPED WITH PHASE GENERATING DEVICE

Inventors: Takayuki Mizuno et al.

Docket No.: 14321.60

54/80

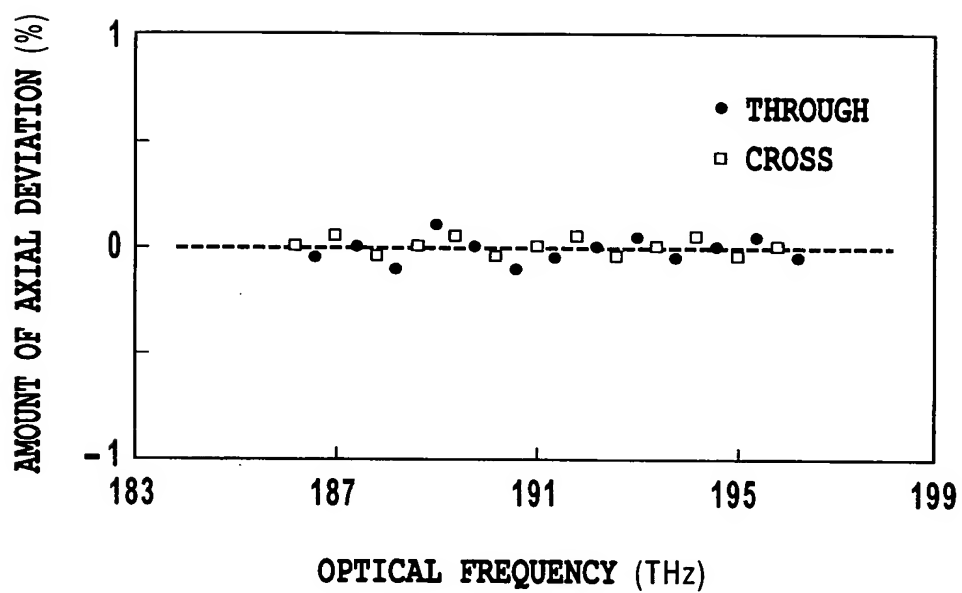


FIG.54

55/80

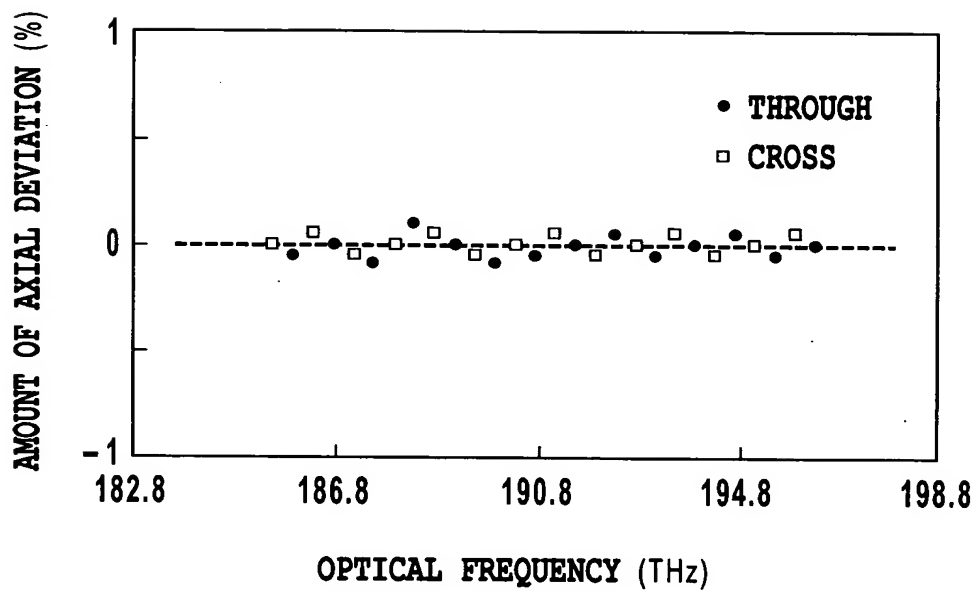


FIG.55

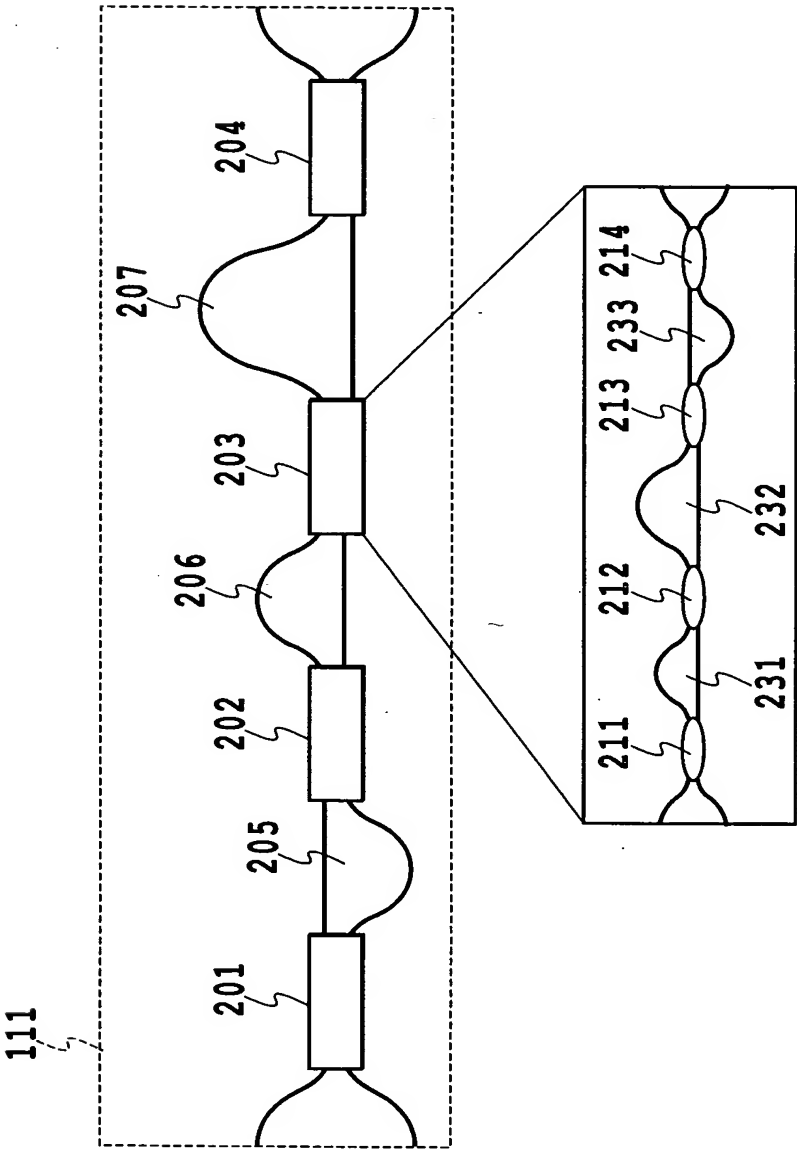


FIG.56

57/80

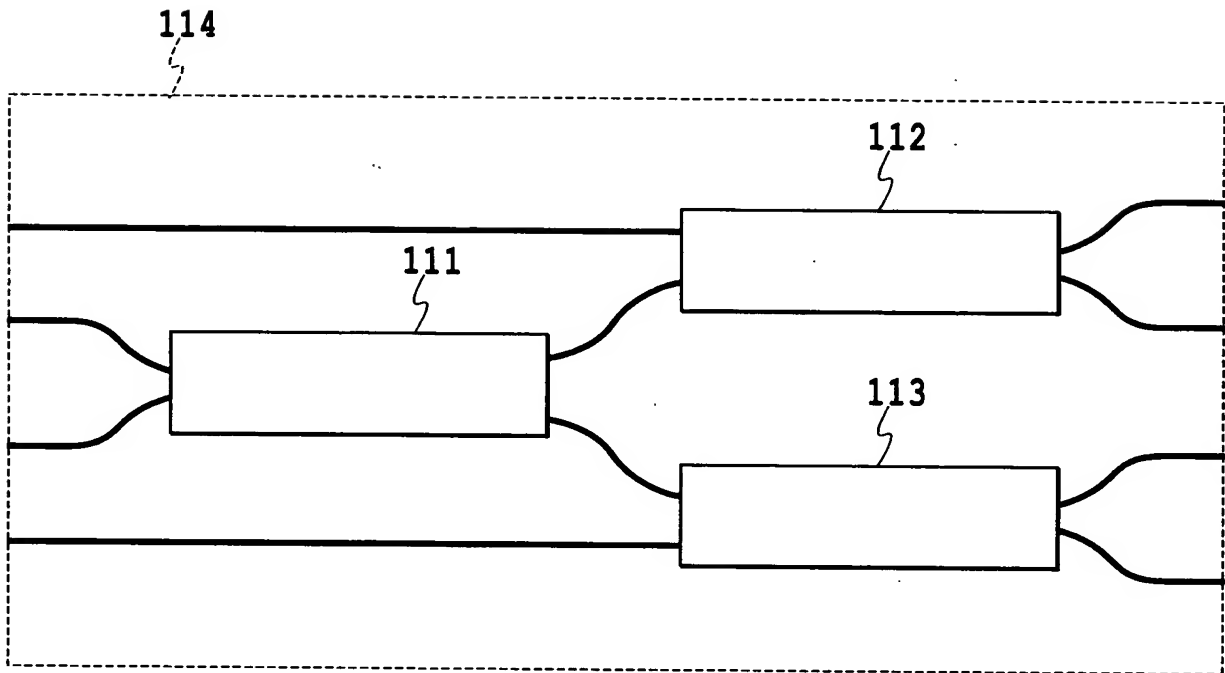


FIG.57

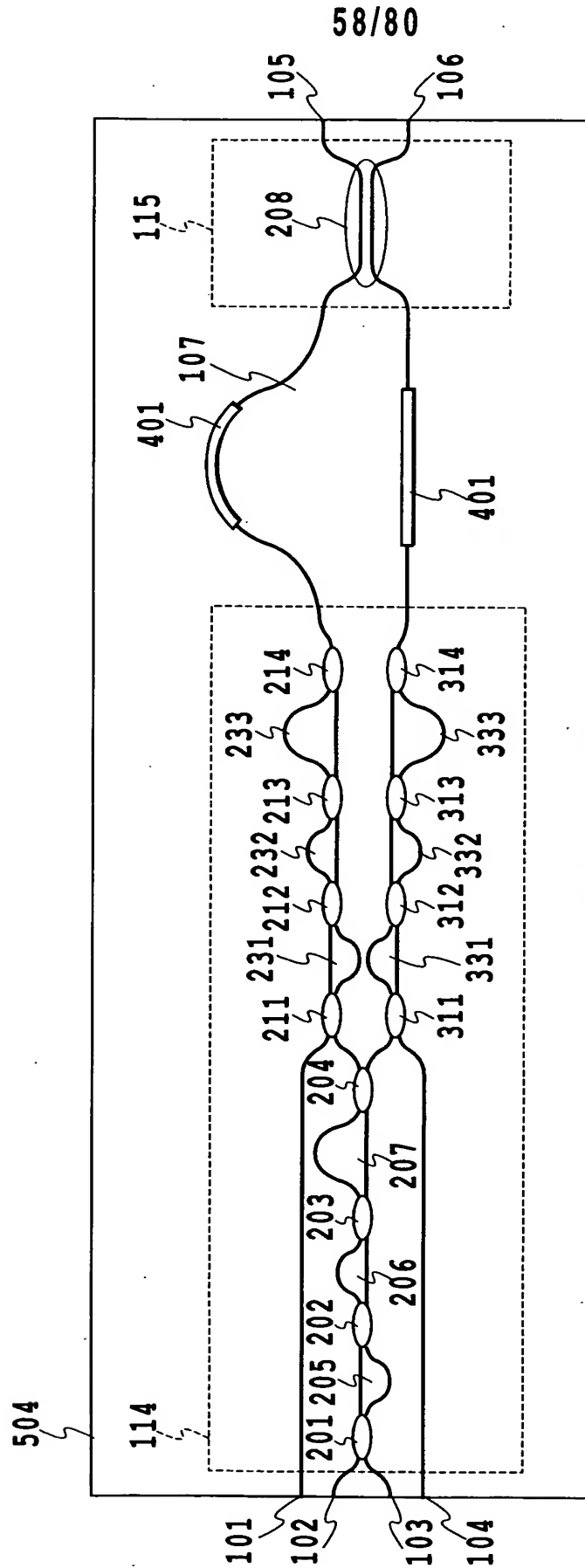


FIG.58

59/80

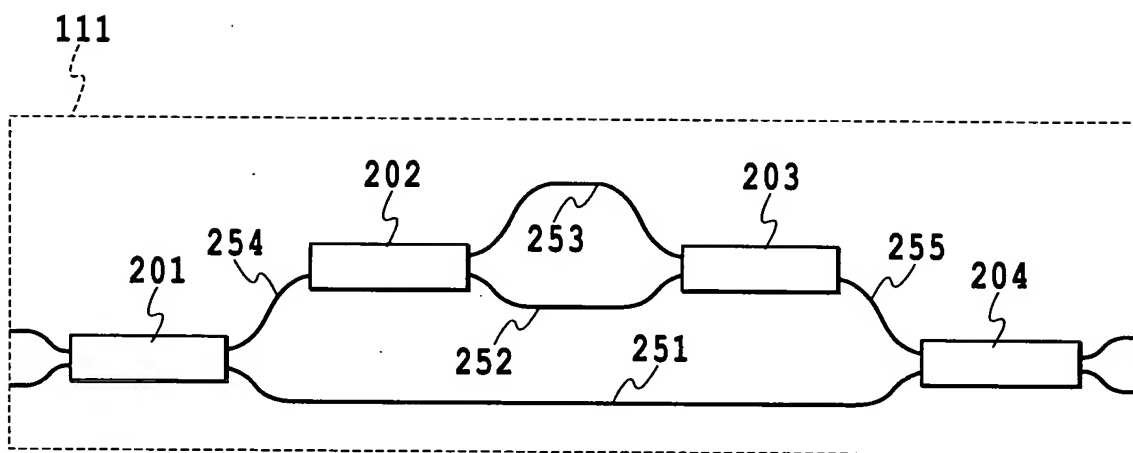


FIG.59

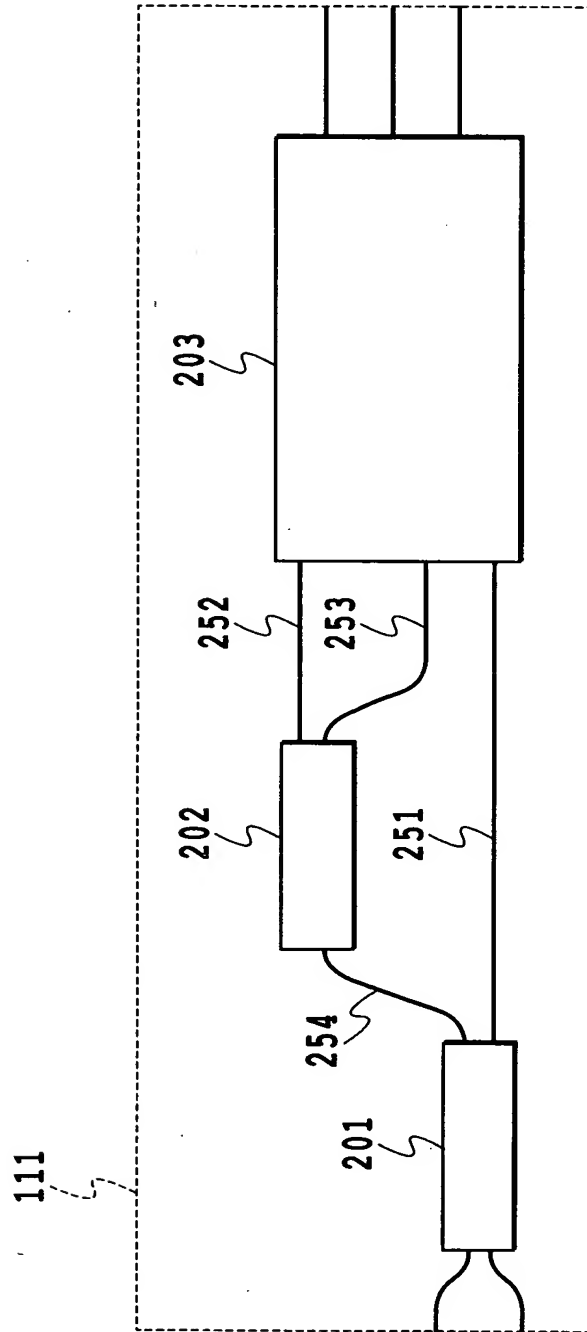


FIG. 60

61/80

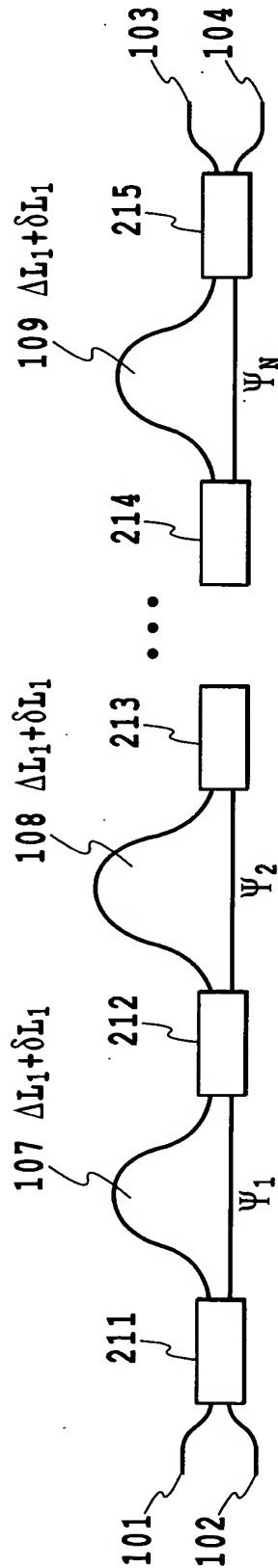


FIG.61

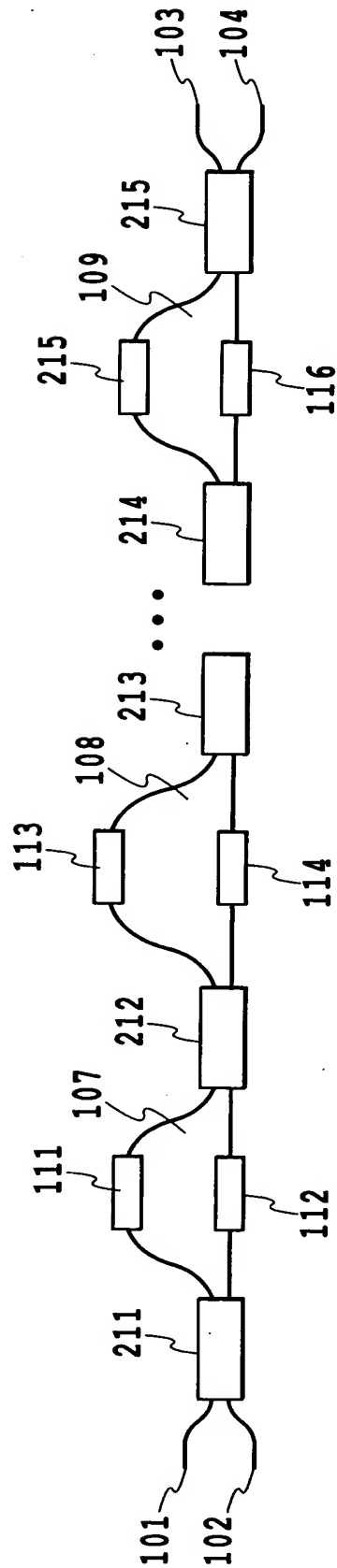


FIG. 62

63/80

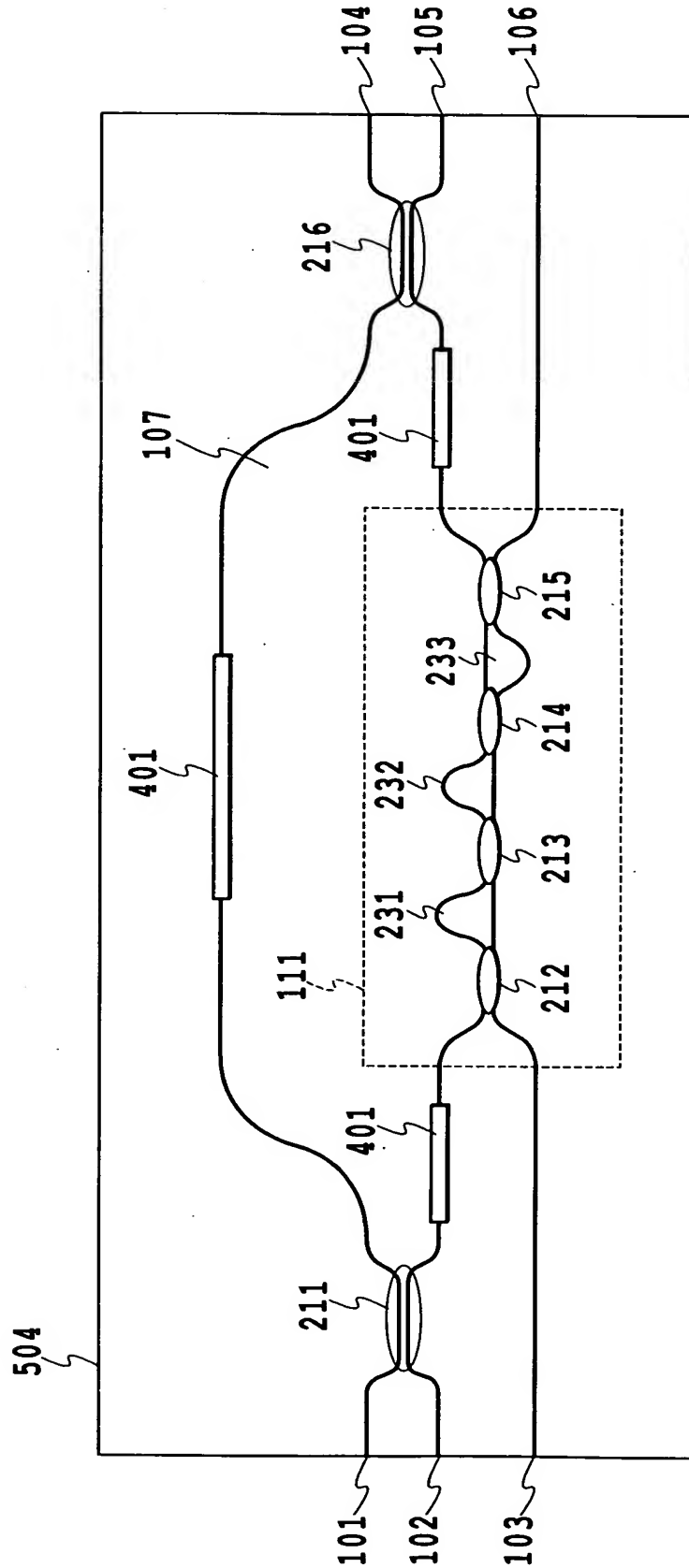


FIG.63

64/80

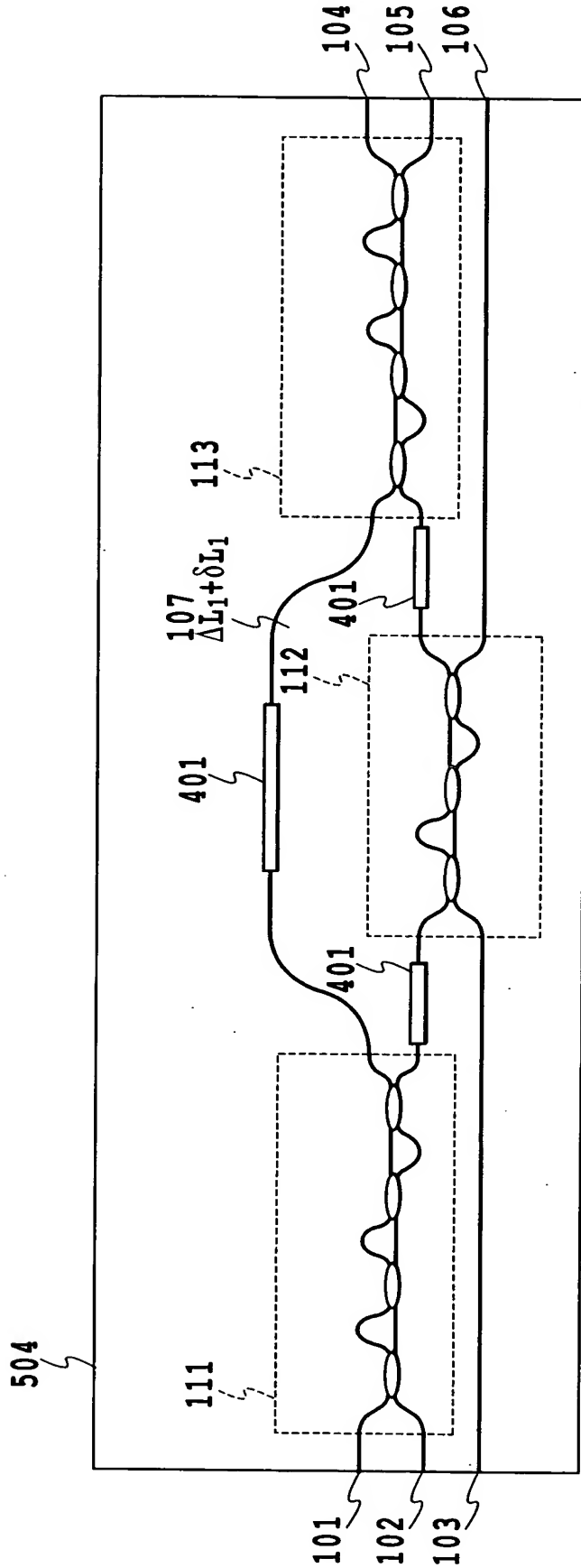


FIG.64

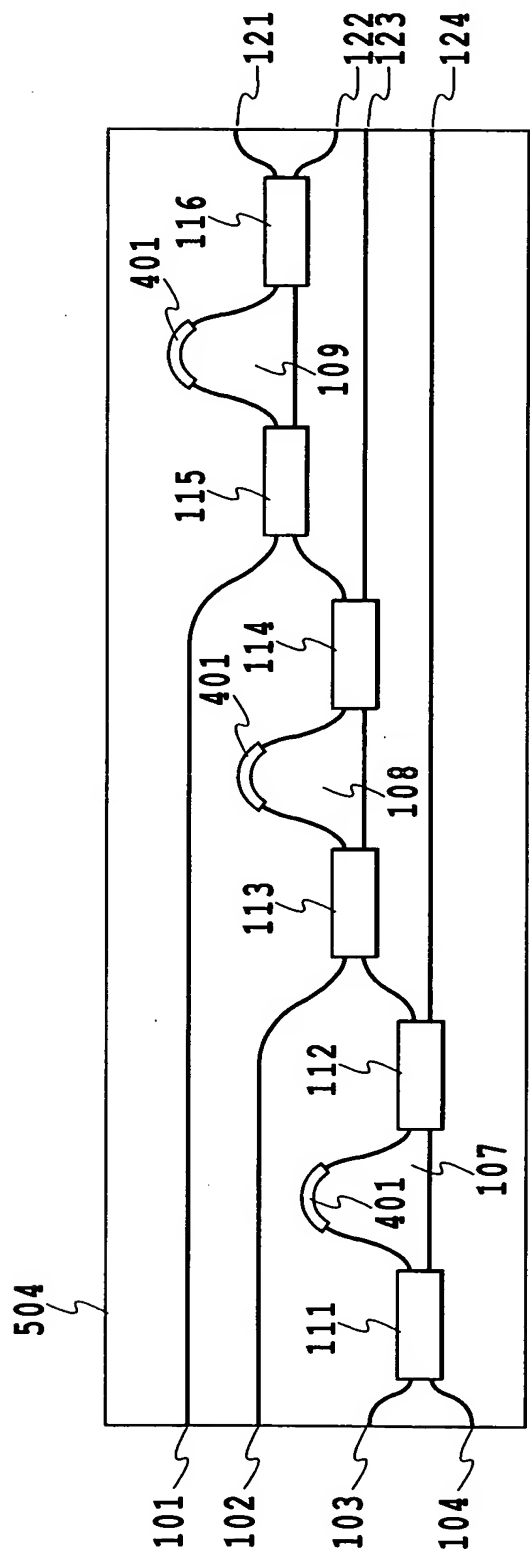


FIG.65

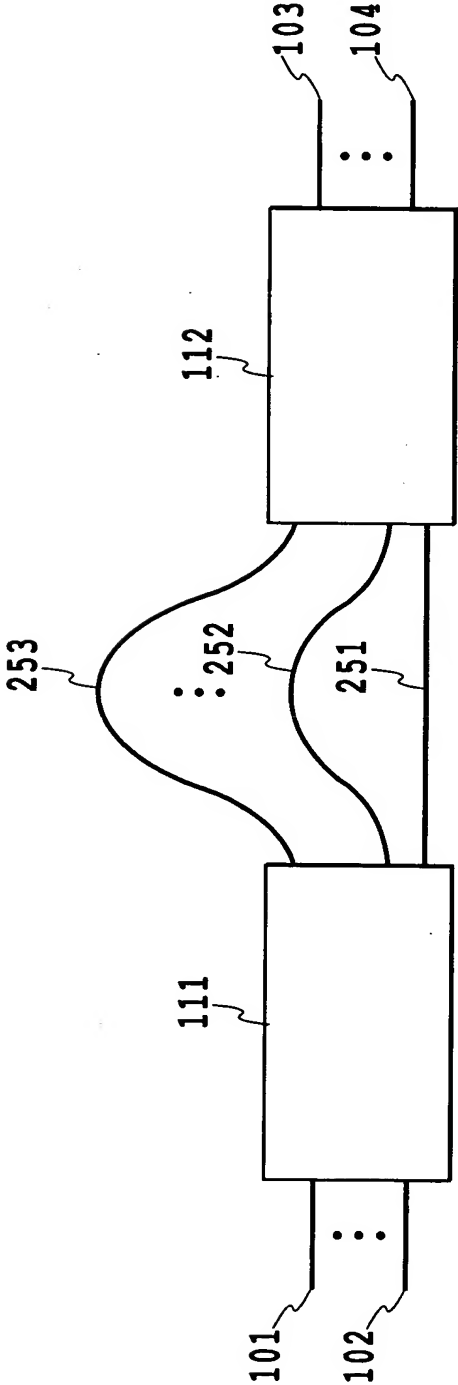


FIG.66

67/80

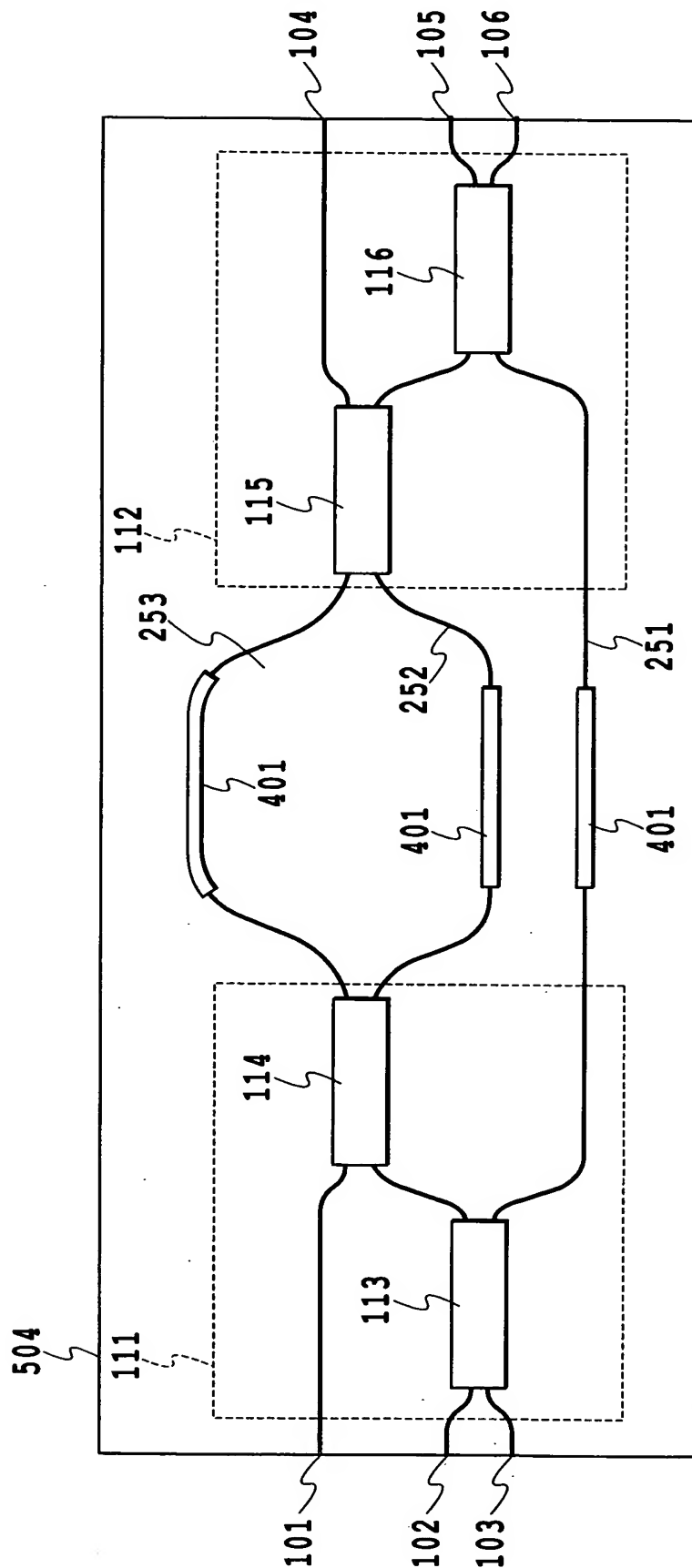


FIG.67

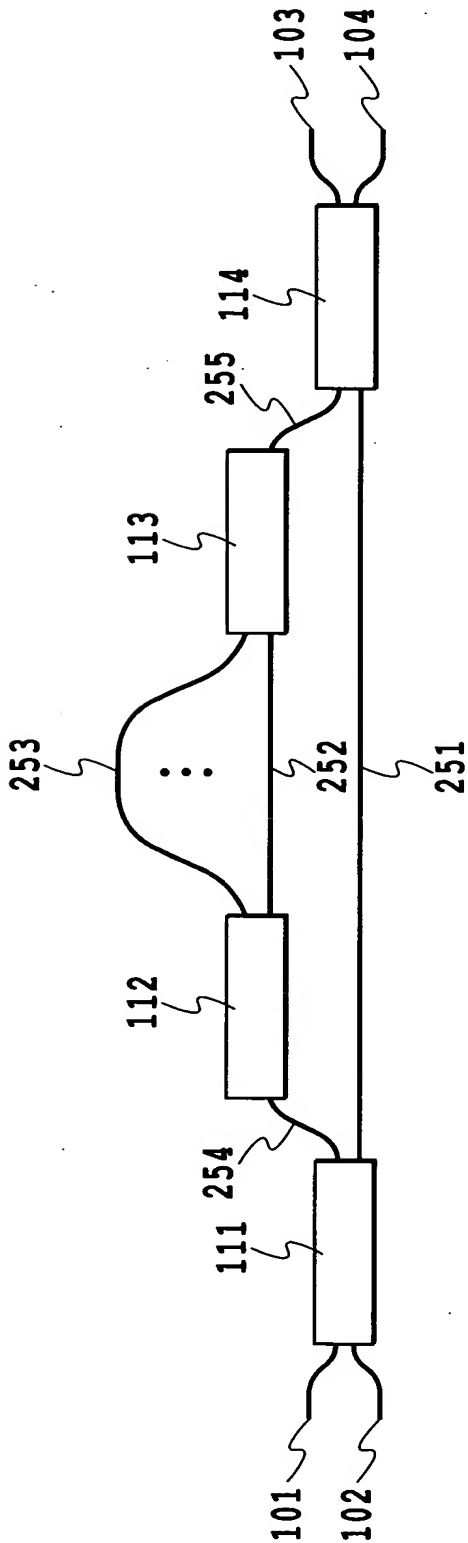


FIG.68

69/80

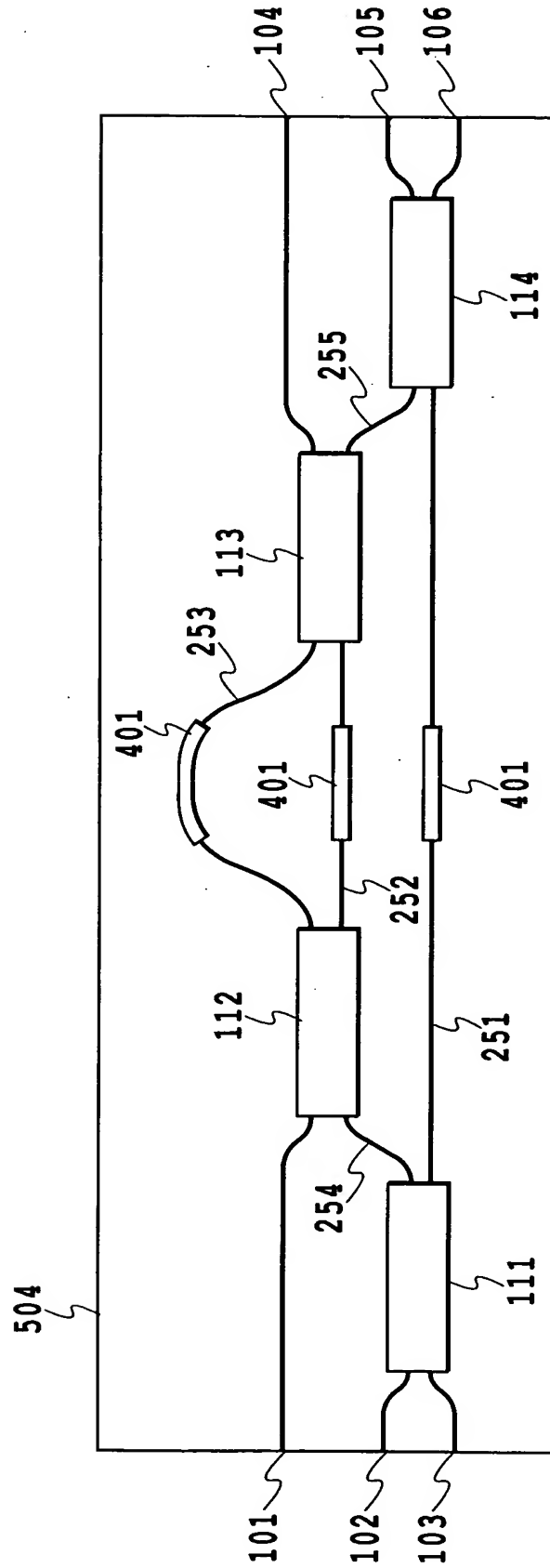


FIG.69

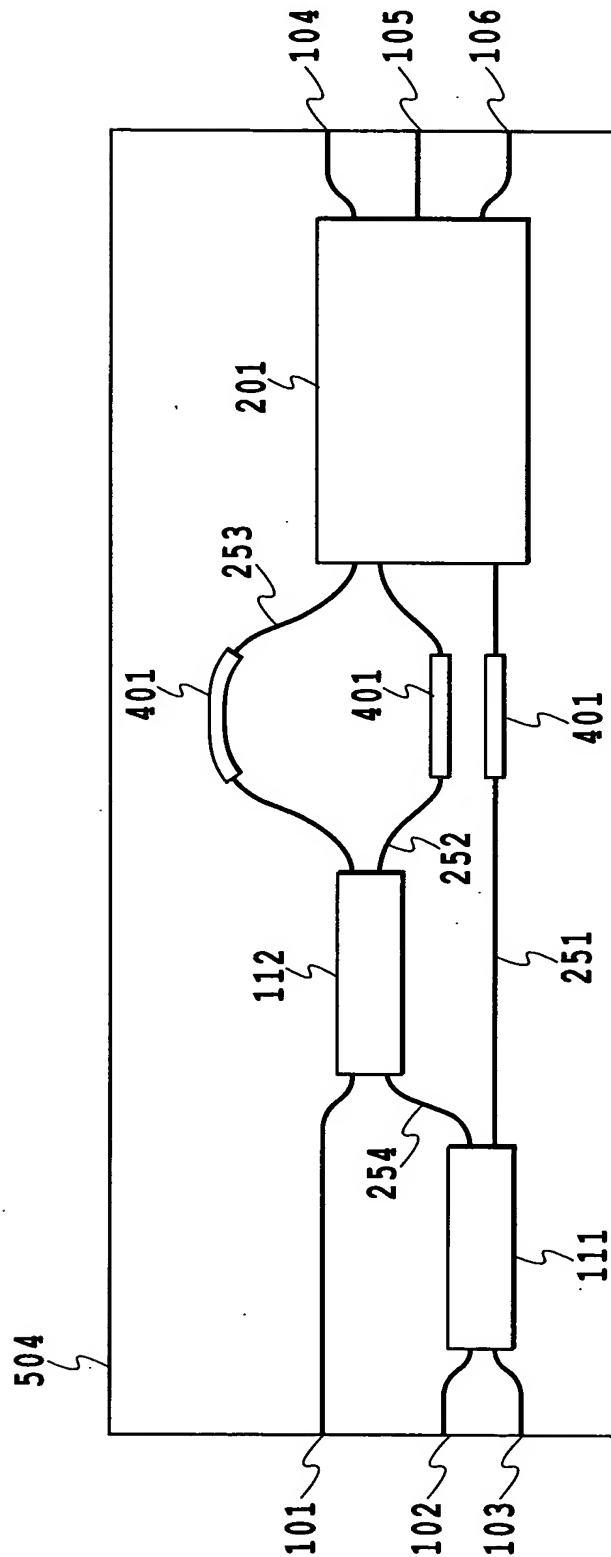


FIG.70

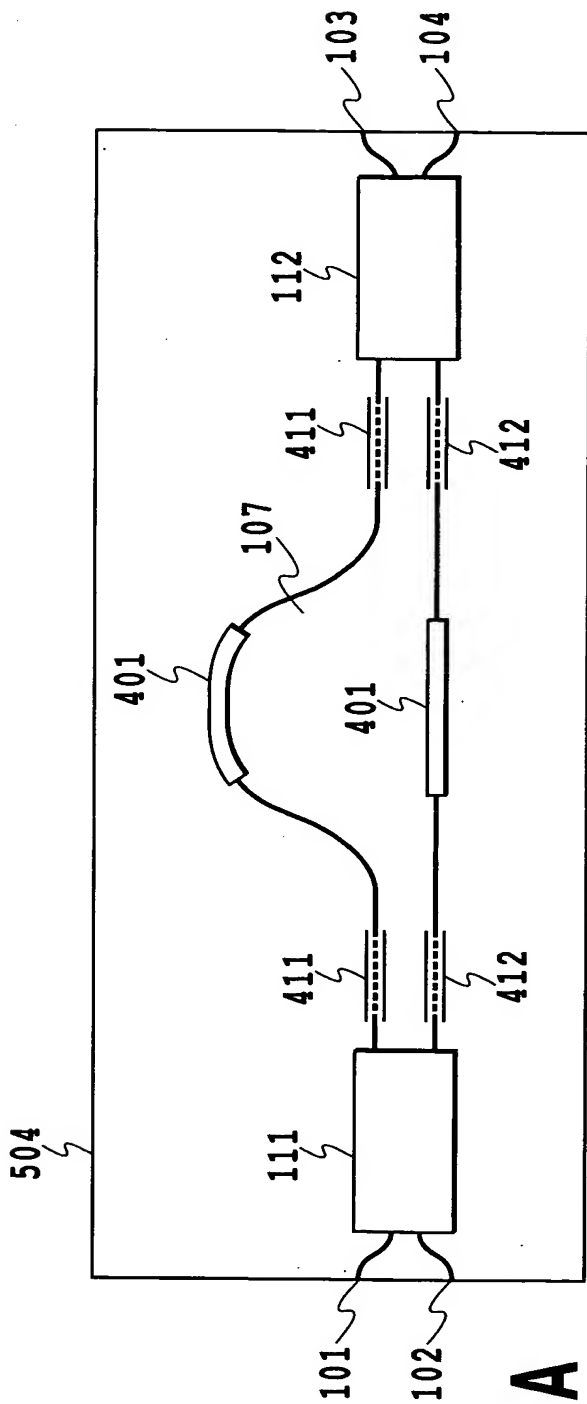


FIG. 71A

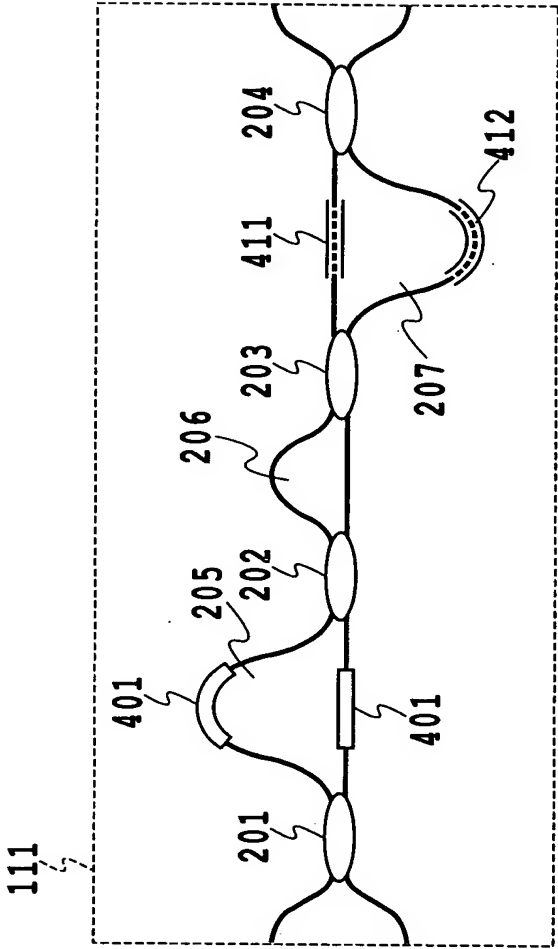


FIG. 71B

72/80

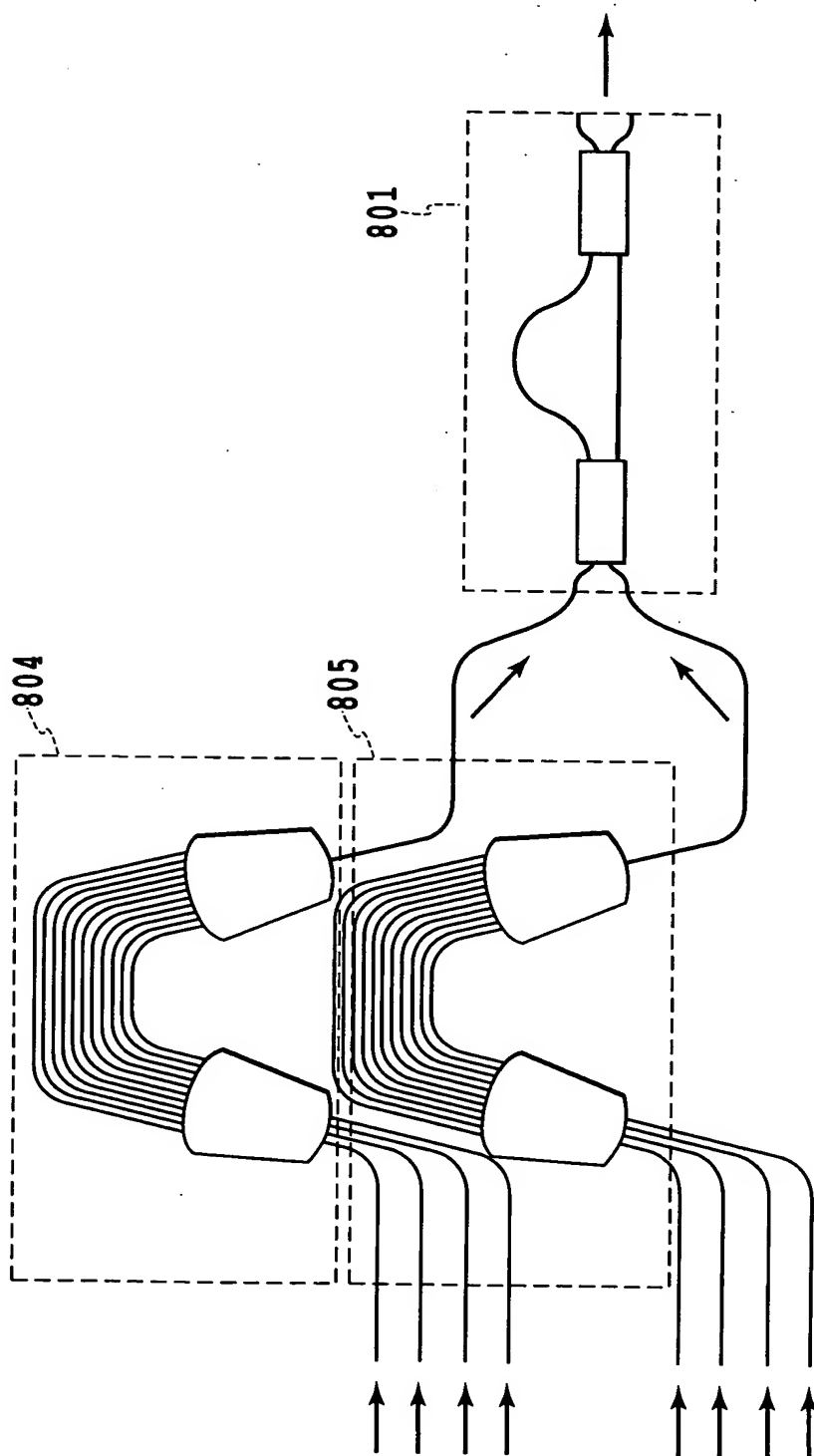


FIG.72

73/80

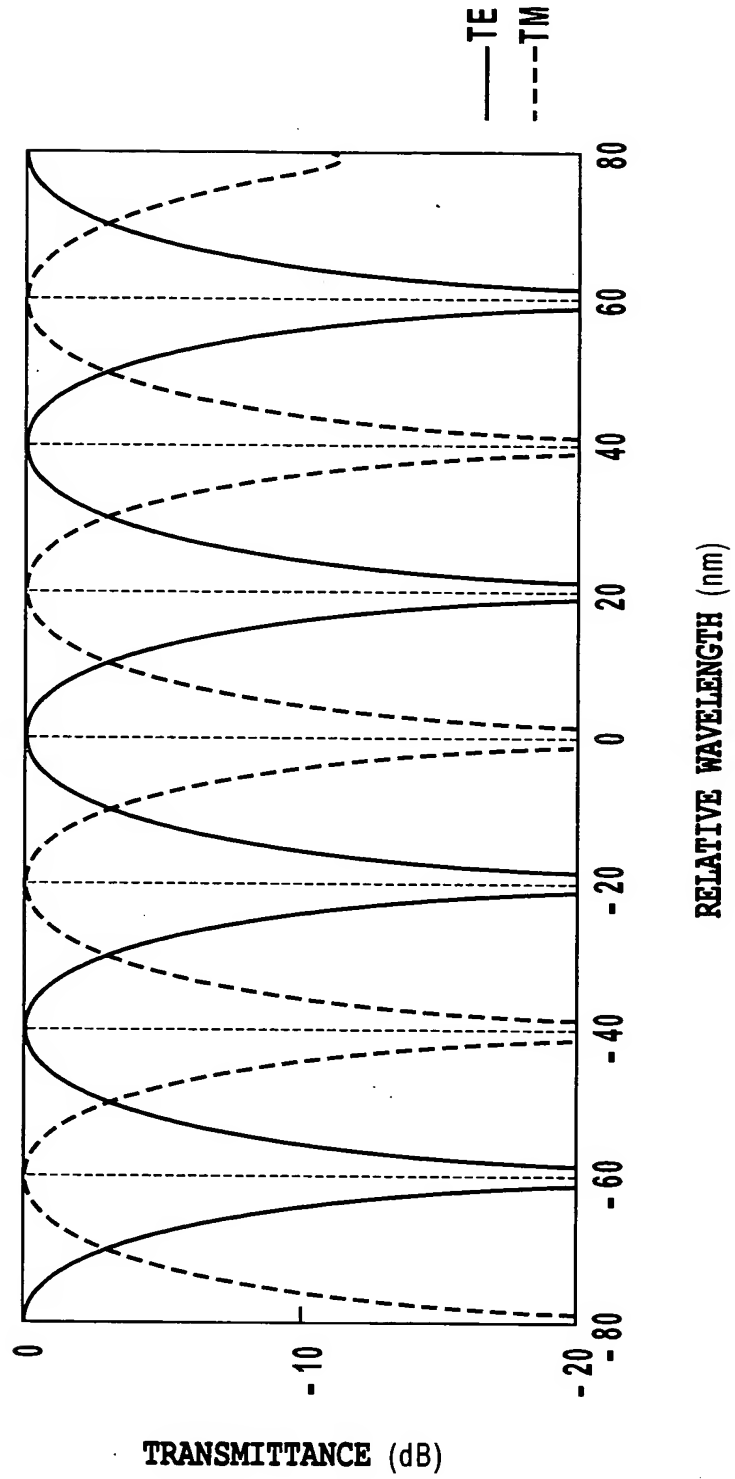


FIG.73

74/80

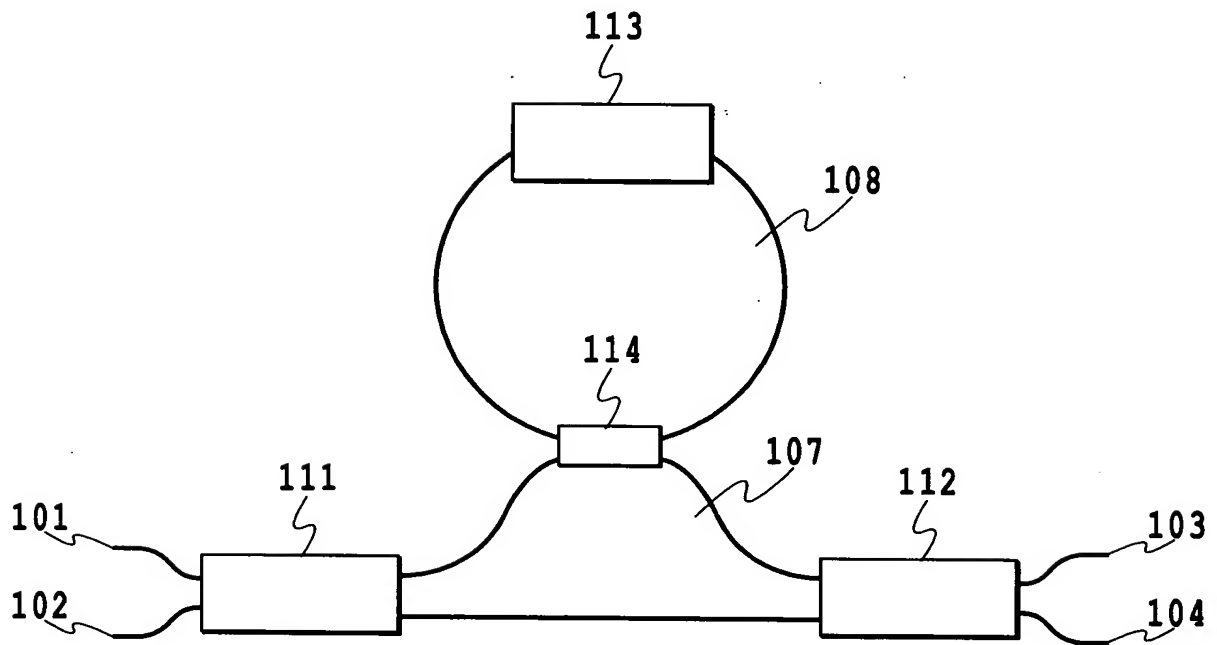


FIG.74

75/80

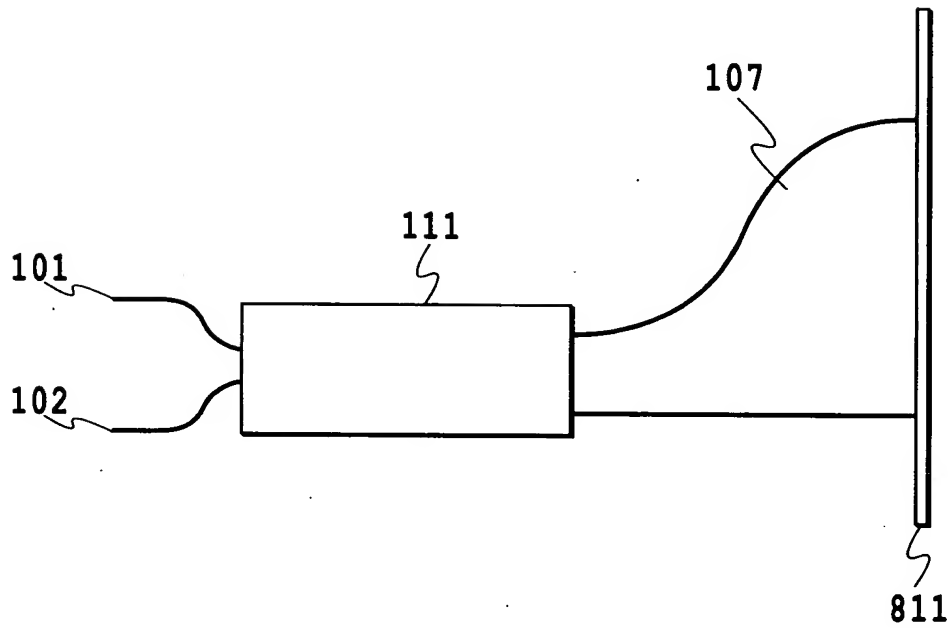


FIG.75

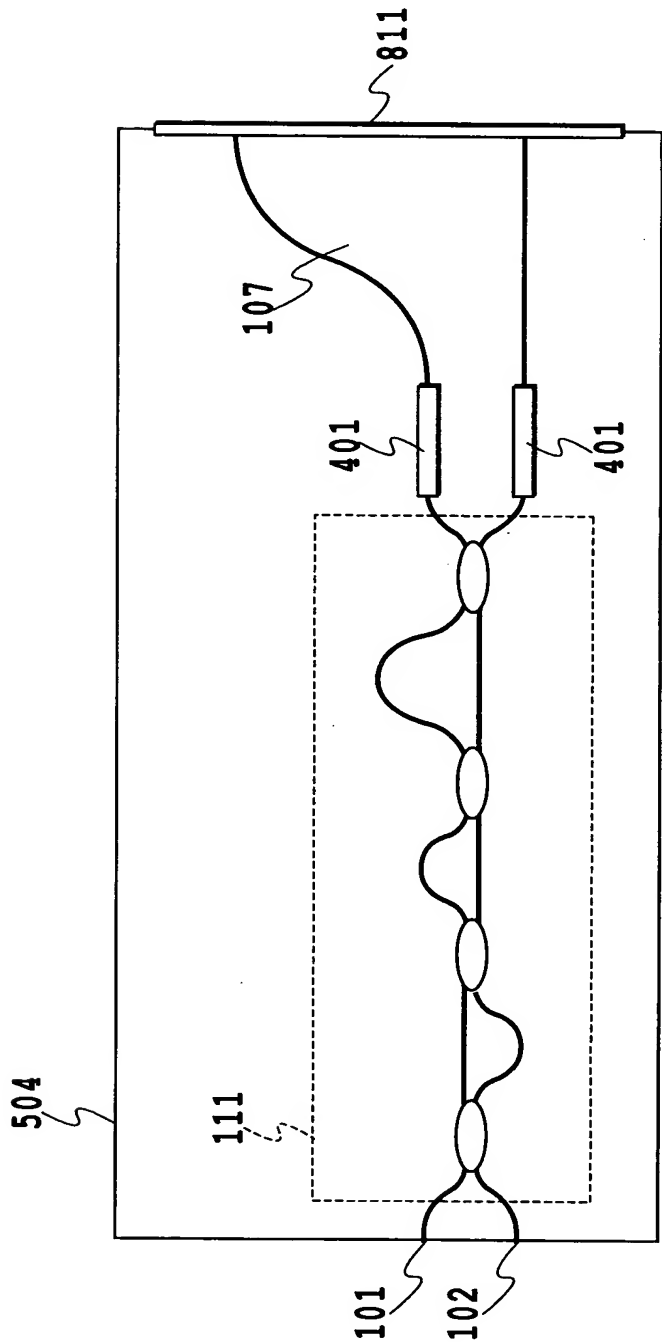


FIG.76

77/80

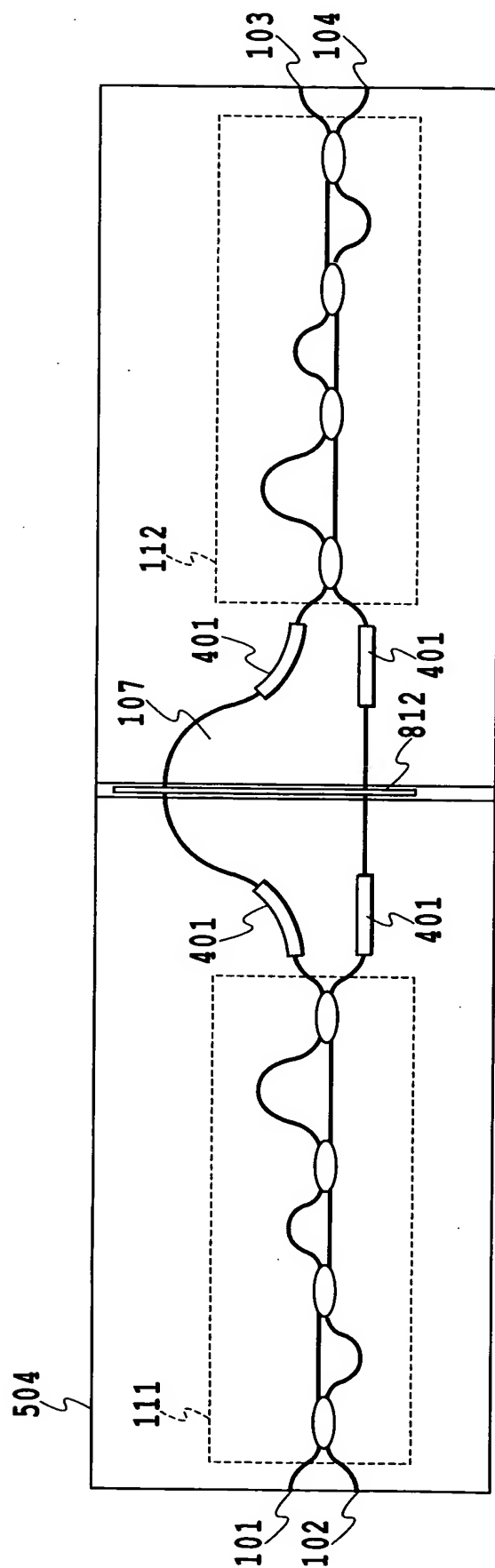


FIG.77

78/80

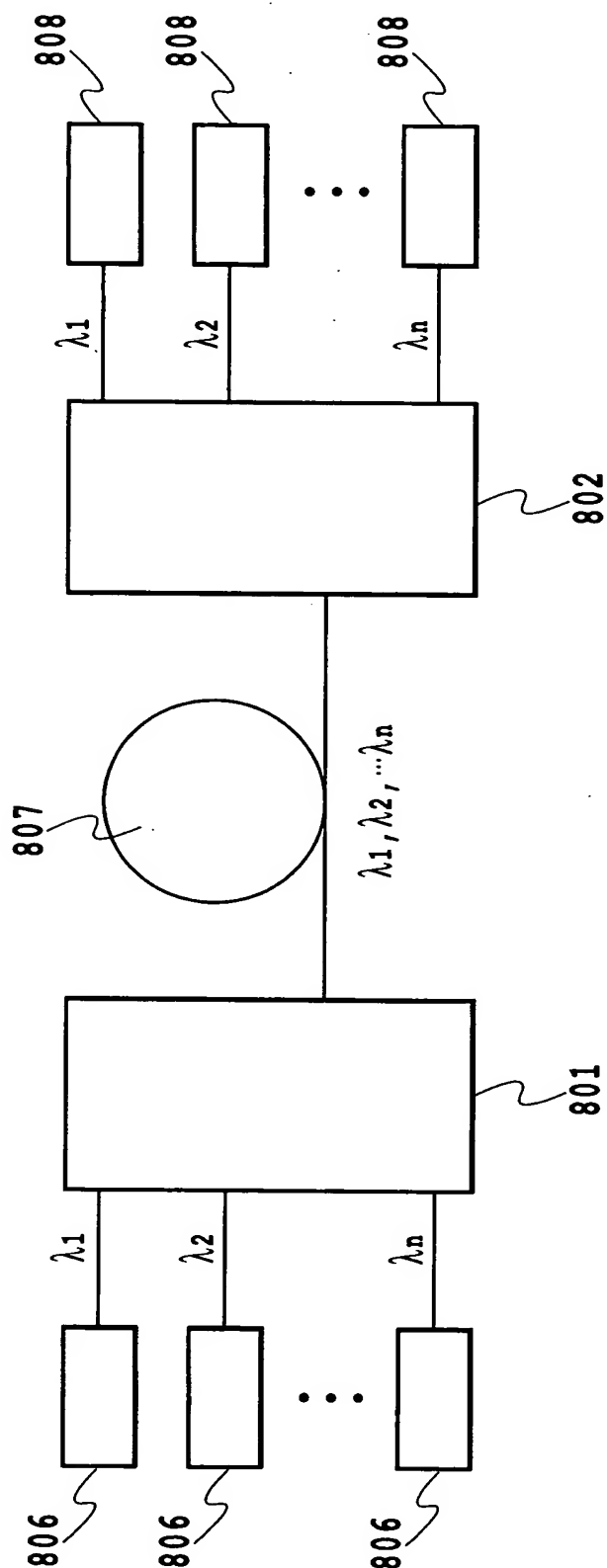


FIG. 78

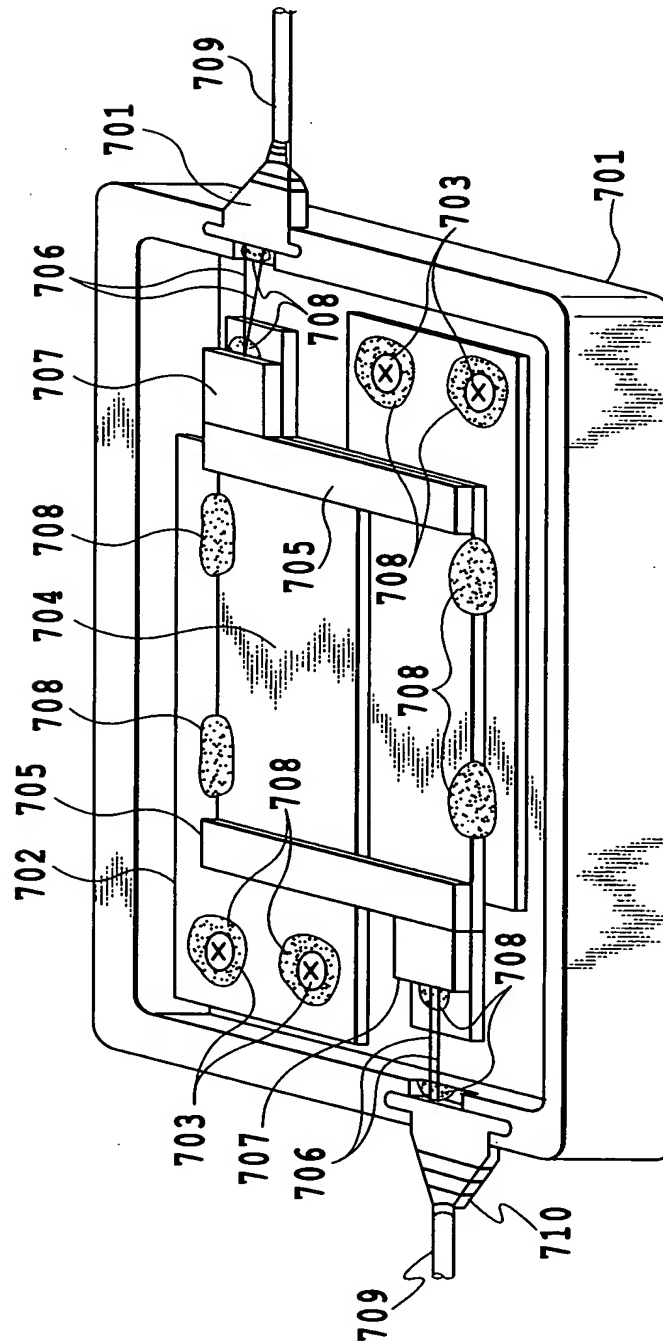


FIG. 79

Title: OPTICAL MULTI/DEMULTIPLEXING CIRCUIT
EQUIPPED WITH PHASE GENERATING DEVICE

Inventors: Takayuki Mizuno et al.

Docket No.: 14321.60

80/80

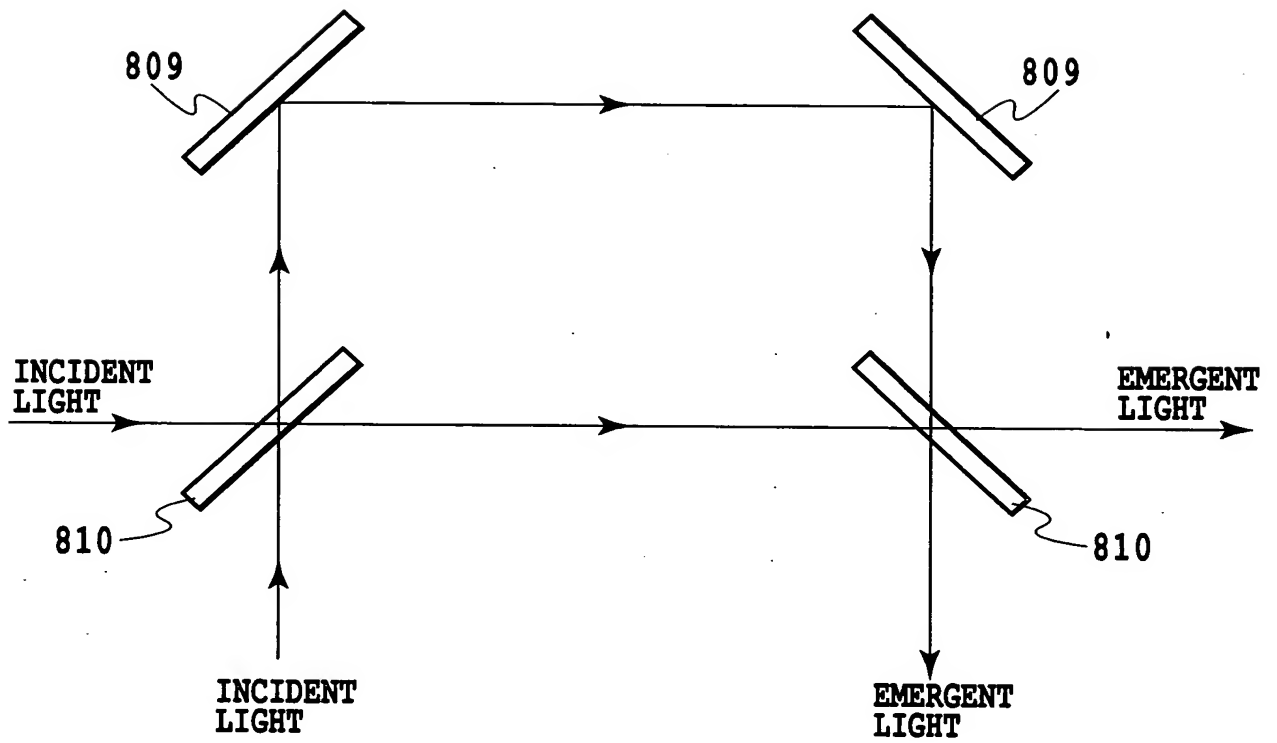


FIG.80